Lectures 25-26 Memory Hierarchy Optimizations & Locality Analysis

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CS745: Memory Hierarchy Optimizations

Caches: A Quick Review

- How do they work?
- Why do we care about them?
- What are typical configurations today?
- What are some important cache parameters that will affect performance?

Optimizing Cache Performance

- Things to enhance:
 - temporal locality
 - spatial locality
- Things to minimize:
 - conflicts (i.e. bad replacement decisions)

What can the *compiler* do to help?



Two Things We Can Manipulate

- Time:
 - When is an object accessed?
- Space:
 - Where does an object exist in the address space?

How do we exploit these two levers?



Time: Reordering Computation

- What makes it difficult to know *when* an object is accessed?
- How can we predict a better time to access it?
 - What information is needed?
- How do we know that this would be safe?



Space: Changing Data Layout

- What do we know about an object's location?
 - scalars, structures, pointer-based data structures, arrays, code, etc.
- How can we tell what a better layout would be?
 - how many can we create?
- To what extent can we safely alter the layout?



Types of Objects to Consider

- Scalars
- Structures & Pointers
- Arrays



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Scalars

- int x; Locals double y; foo(int a){ Globals ulletint i; ••• Procedure arguments • x = a*i;••• Is cache performance a concern here? } •
 - If so, what can be done?



Structures and Pointers

•	What	can	we	do	here?	
---	------	-----	----	----	-------	--

- within a node
- across nodes

struct {
 int count;
 double velocity;
 double inertia;
 struct node *neighbors[N];
} node;

• What limits the compiler's ability to optimize here?

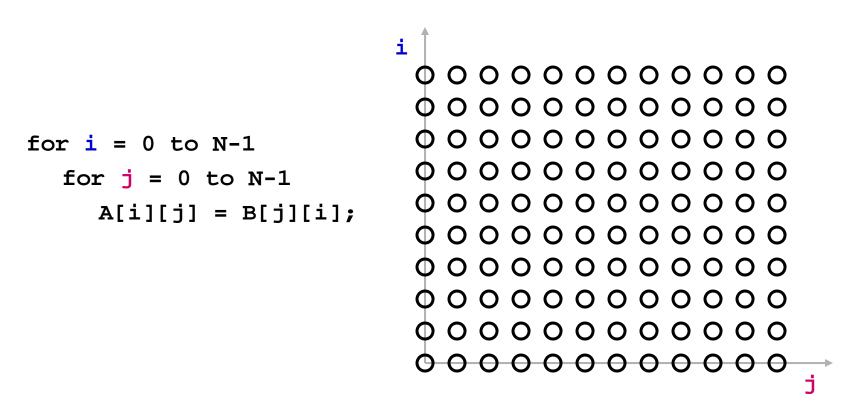


<u>Arrays</u>

```
double A[N][N], B[N][N];
...
for i = 0 to N-1
  for j = 0 to N-1
        A[i][j] = B[j][i];
```

- usually accessed within loops nests
 - makes it easy to understand "time"
- what we know about array element addresses:
 - start of array?
 - relative position within array

Handy Representation: "Iteration Space"

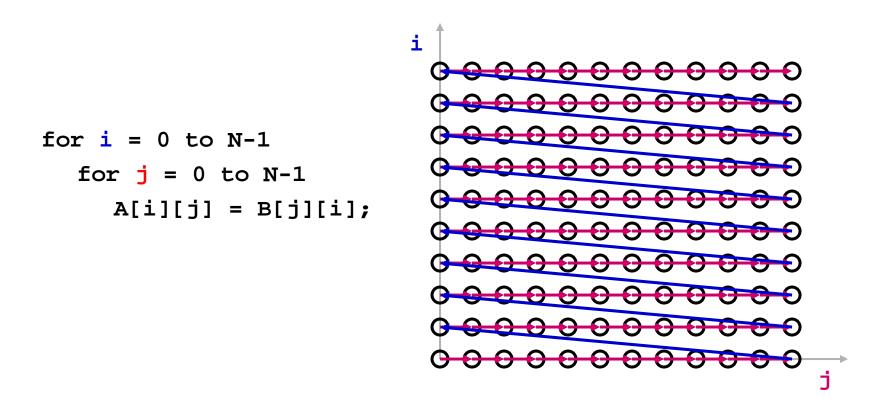


• each position represents an iteration

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Visitation Order in Iteration Space

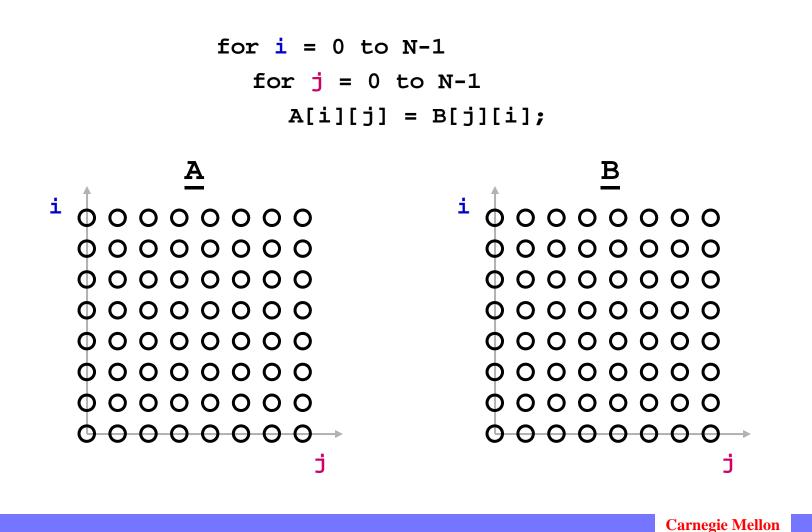


• Note: iteration space ≠ data space

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When Do Cache Misses Occur?



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When Do Cache Misses Occur?

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Optimizing the Cache Behavior of Array Accesses

- We need to answer the following questions:
 - when do cache misses occur?
 - use "locality analysis"
 - can we change the order of the iterations (or possibly data layout) to produce better behavior?
 - evaluate the cost of various alternatives
 - does the new ordering/layout still produce correct results?
 - use "dependence analysis"

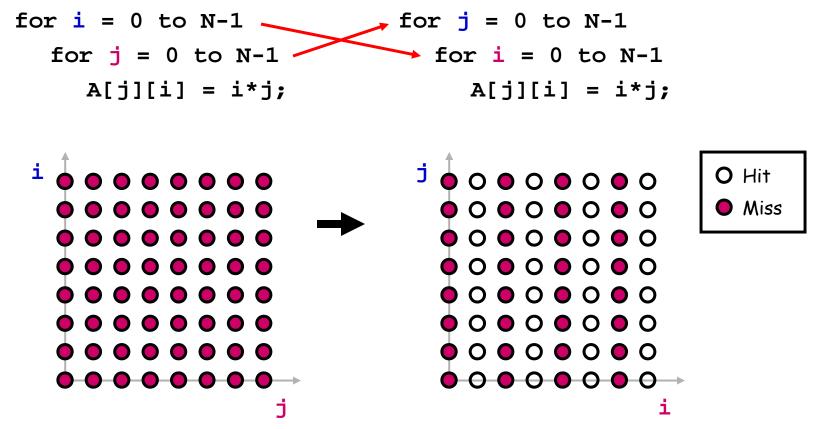
Examples of Loop Transformations

- Loop Interchange
- Cache Blocking
- Skewing
- Loop Reversal
- ...

(we will briefly discuss the first two)



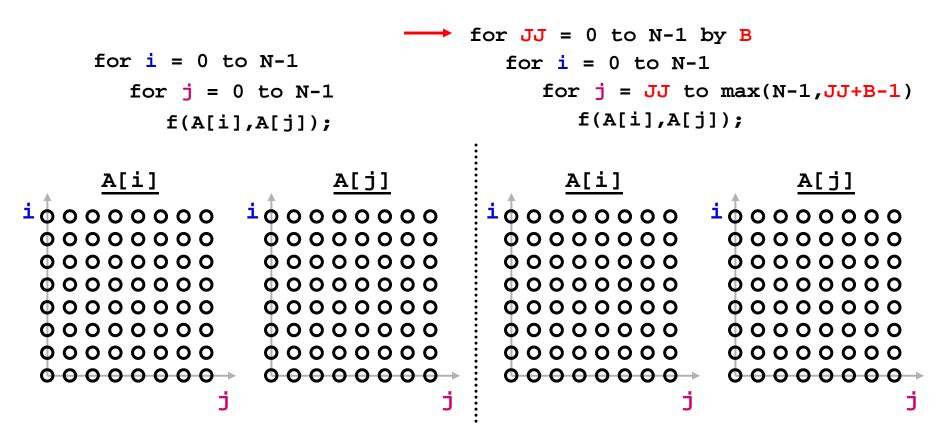
Loop Interchange



• (assuming N is large relative to cache size)



Cache Blocking (aka "Tiling")



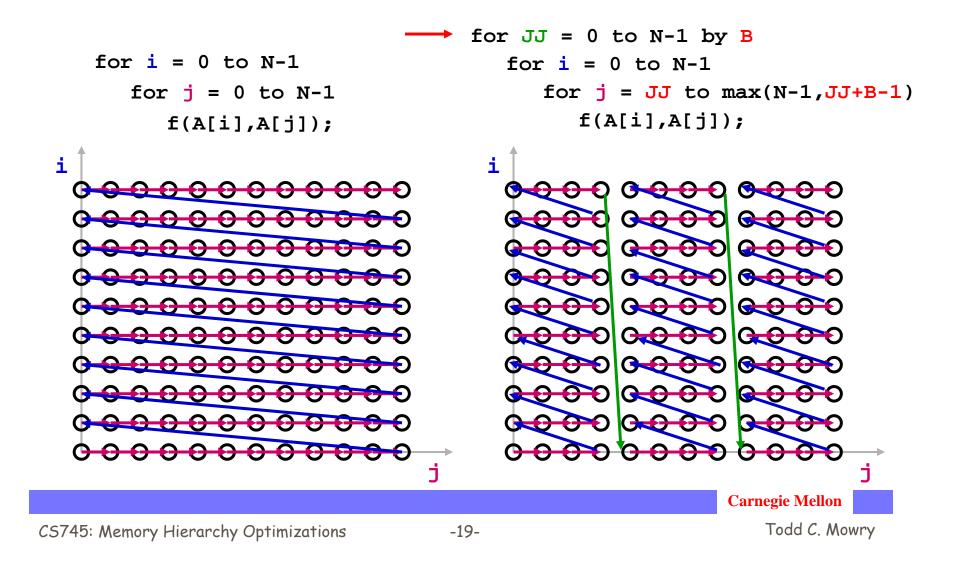
now we can exploit temporal locality

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Impact on Visitation Order in Iteration Space



Cache Blocking in Two Dimensions

- brings square sub-blocks of matrix "ъ" into the cache
- completely uses them up before moving on

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Predicting Cache Behavior through "Locality Analysis"

- Definitions:
 - <u>Reuse</u>:
 - accessing a location that has been accessed in the past
 - Locality:
 - accessing a location that is now found in the cache
- Key Insights
 - Locality only occurs when there is reuse!
 - BUT, reuse does not necessarily result in locality.
 - why not?

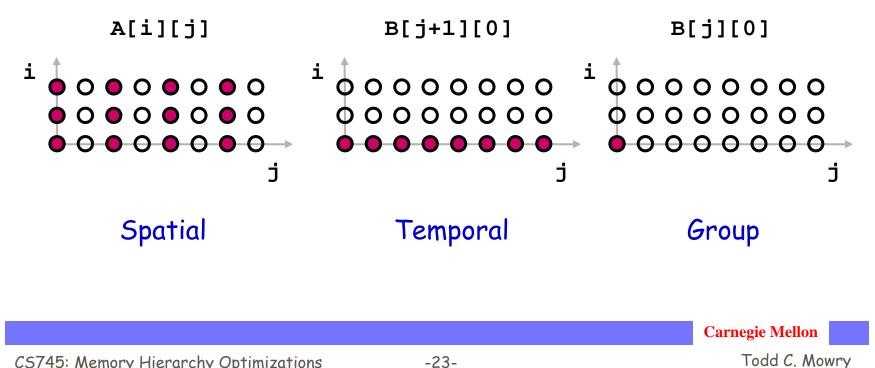


Steps in Locality Analysis

- 1. Find data reuse
 - if caches were infinitely large, we would be finished
- 2. Determine "localized iteration space"
 - set of inner loops where the data accessed by an iteration is expected to fit within the cache
- 3. Find data locality:
 - reuse \cap localized iteration space \Rightarrow locality

Types of Data Reuse/Locality





Reuse Analysis: Representation

• Map *n* loop indices into *d* array indices via array indexing function:

$$\vec{f}(\vec{i}) = H\vec{i} + \vec{c}$$

$$A[i][j] = A\left(\begin{bmatrix}1 & 0\\ 0 & 1\end{bmatrix}\begin{bmatrix}i\\j\end{bmatrix} + \begin{bmatrix}0\\ 0\end{bmatrix}\right)$$

$$B[j][0] = B\left(\begin{bmatrix}0 & 1\\ 0 & 0\end{bmatrix}\begin{bmatrix}i\\j\end{bmatrix} + \begin{bmatrix}0\\ 0\end{bmatrix}\right)$$

$$B[j+1][0] = B\left(\begin{bmatrix}0 & 1\\ 0 & 0\end{bmatrix}\begin{bmatrix}i\\j\end{bmatrix} + \begin{bmatrix}1\\ 0\end{bmatrix}\right)$$

Finding Temporal Reuse

• Temporal reuse occurs between iterations $\vec{i_1}$ and $\vec{i_2}$ whenever: $H\vec{i_1} + \vec{c} = H\vec{i_2} + \vec{c}$ $H(\vec{i_1} - \vec{i_2}) = \vec{0}$

• Rather than worrying about individual values of
$$\vec{i}_1$$
 and \vec{i}_2 , we say that reuse occurs along direction vector \vec{r} when:

$$H(\vec{r}) = \vec{0}$$

• Solution: compute the *nullspace* of *H*

Temporal Reuse Example

• Reuse between iterations (i_1, j_1) and (i_2, j_2) whenever:

$$\begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_1 \\ j_1 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_2 \\ j_2 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$
$$\begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} i_1 - i_2 \\ j_1 - j_2 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \end{bmatrix}$$

- True whenever $j_1 = j_2$, and regardless of the difference between i_1 and i_2 .
 - i.e. whenever the difference lies along the nullspace of $\begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix}$, which is span{(1,0)} (i.e. the outer loop).

More Complicated Example

• Nullspace of
$$\begin{bmatrix} 1 & 1 \\ 0 & 0 \end{bmatrix}$$
 = span{(1,-1)}.

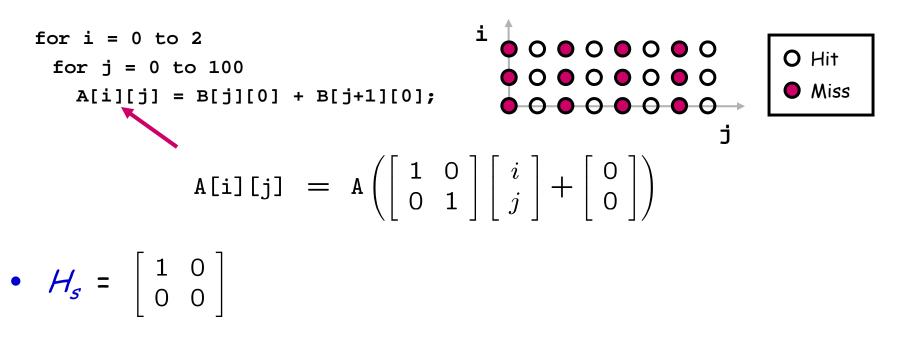
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Computing Spatial Reuse

- Replace last row of *H* with zeros, creating *H*_s
- Find the nullspace of H_s
- <u>Result</u>: vector along which we access the same row



Computing Spatial Reuse: Example



- Nullspace of $H_s = span\{(0,1)\}$
 - i.e. access same row of A[i][j] along inner loop

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<u>Computing Spatial Reuse: More Complicated Example</u>

for
$$\mathbf{i} = 0$$
 to N-1
for $\mathbf{j} = 0$ to N-1
A[$\mathbf{i}+\mathbf{j}$] = $\mathbf{i}*\mathbf{j}$;
A[$\mathbf{i}+\mathbf{j}$] = A $\left(\begin{bmatrix} 1 & 1 \end{bmatrix} \begin{bmatrix} i \\ j \end{bmatrix} + \begin{bmatrix} 0 \end{bmatrix} \right)$
 $\mathcal{H}_{S} = \begin{bmatrix} 0 & 0 \end{bmatrix}$
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- Nullspace of H = span{(1,-1)}
- Nullspace of $H_s = \text{span}\{(1,0),(0,1)\}$ $\uparrow \longrightarrow$

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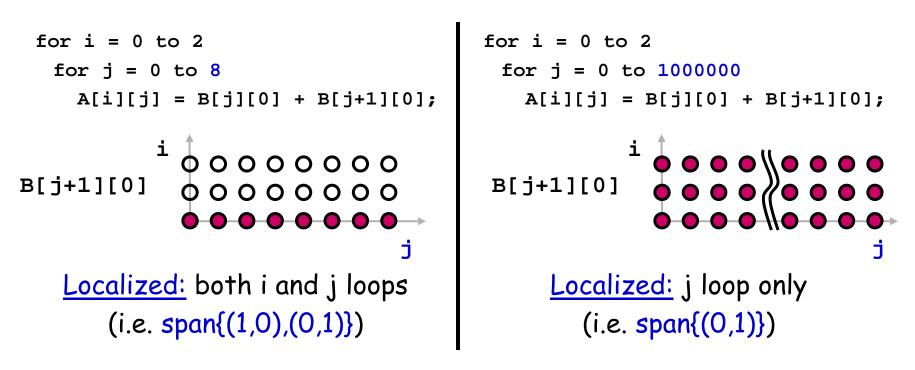
Group Reuse

- Only consider "uniformly generated sets"
 - index expressions differ only by constant terms
- Check whether they actually do access the same cache line
- Only the "leading reference" suffers the bulk of the cache misses

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Localized Iteration Space

• Given finite cache, when does reuse result in locality?



• Localized if accesses less data than *effective cache size*

Computing Locality

- Reuse Vector Space \cap Localized Vector Space \Rightarrow Locality Vector Space
- <u>Example</u>: for i = 0 to 2
 for j = 0 to 100
 A[i][j] = B[j][0] + B[j+1][0];
- If both loops are localized:
 - $span{(1,0)} \cap span{(1,0),(0,1)} \Rightarrow span{(1,0)}$
 - i.e. temporal reuse *does* result in temporal locality
- If only the innermost loop is localized:
 - $span\{(1,0)\} \cap span\{(0,1)\} \Rightarrow span\{\}$
 - i.e. no temporal locality