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Pixel Planes and Pixel Flow (UNC) http://www.cs.unc.edu/~pxfl/ programmable processor per pixel good for programmable shading, image processing can be used for rasterization Pixel-Planes 4: 512x512 processors with 72bits of memory But most processors idle for most triangles Pixel-Planes 5: divide screen into ~20 tiles each with a bank of processors. Network is limit. 2Million tri/sec.

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Talisman (Microsoft)

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http://research.microsoft.com/MSRSIGGRAPH/96/Talisman/

Observation: an image is usually much like the one that preceded it in an animation.

Goal: a \$200-300 board

image-based rendering

cache images of rendered geometry

re-use with affine image warping (sophisticated sprites) re-render only when necessary to reduce bandwidth and computational cost

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Current & Future Issues

- interaction
- geometry compression
- progressive transmission
- alternative modeling schemes (not polygon soup) parametric surfaces, implicit surfaces, subdivision surfaces generalized texture mapping: displacement mapping, light mapping

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programmable shaders • beyond just geometry:

dynamics, collision detection, AI?

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