15-213 "The Class That Gives CMU Its Zip!"

Bits and Bytes Aug. 29, 2002

Topics

- Why bits?
- Representing information as bits
 - Binary/Hexadecimal
 - Byte representations
 - » numbers
 - » characters and strings
 - » Instructions
- Bit-level manipulations
 - Boolean algebra
 - Expressing in C

class02.ppt

Why Don't Computers Use Base 10?

Base 10 Number Representation

- That's why fingers are known as "digits"
- Natural representation for financial transactions
 - Floating point number cannot exactly represent \$1.20
- Even carries through in scientific notation
 - 1.5213 X 10⁴

Implementing Electronically

- Hard to store
 - ENIAC (First electronic computer) used 10 vacuum tubes / digit
- Hard to transmit
 - Need high precision to encode 10 signal levels on single wire
- Messy to implement digital logic functions
 - Addition, multiplication, etc.

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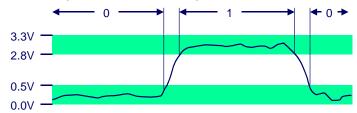
Binary Representations

Base 2 Number Representation

- Represent 15213₁₀ as 11101101101101₂
- Represent 1.20₁₀ as 1.0011001100110011[0011]...₂
- Represent 1.5213 X 10⁴ as 1.1101101101101₂ X 2¹³

Electronic Implementation

- Easy to store with bistable elements
- Reliably transmitted on noisy and inaccurate wires



Byte-Oriented Memory Organization

Programs Refer to Virtual Addresses

- Conceptually very large array of bytes
- Actually implemented with hierarchy of different memory types
 - SRAM, DRAM, disk
 - Only allocate for regions actually used by program
- In Unix and Windows NT, address space private to particular "process"
 - Program being executed
 - Program can clobber its own data, but not that of others

Compiler + Run-Time System Control Allocation

- Where different program objects should be stored
- Multiple mechanisms: static, stack, and heap
- In any case, all allocation within single virtual address space

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Encoding Byte Values

Byte = 8 bits

- Binary 00000000₂ to 11111111₂
- Decimal: 0_{10} to 255_{10}
- Hexadecimal 00₁₆ to FF₁₆
 - Base 16 number representation
 - Use characters '0' to '9' and 'A' to 'F'
 Write FA1D37B₁₆ in C as 0xFA1D37B

»	O	r	C	xf	a	1	d	3	7]	C

He	t De	cimal Binary
0	0	0000
2 3	2 3	0001
2	2	0010
3	3	0011
4 5 6 7	4 5 6 7	0100
5	5	0101
6	6	0110
7		0111
8	8	1000
9	9	1001
Α	10	1010
В	11	1011
С	12	1100
D	13	1101
E	14	1110
F	15	1111

Machine Words

Machine Has "Word Size"

- Nominal size of integer-valued data
 - Including addresses
- Most current machines are 32 bits (4 bytes)
 - Limits addresses to 4GB
 - Becoming too small for memory-intensive applications
- High-end systems are 64 bits (8 bytes)
 - Potentially address ≈ 1.8 X 10¹⁹ bytes
- Machines support multiple data formats
 - Fractions or multiples of word size
 - Always integral number of bytes

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Word-Oriented Memory Organization

Addresses Specify Byte Locations

- Address of first byte in word
- Addresses of successive words differ by 4 (32-bit) or 8 (64-bit)

32-bit Words	64-bit Words	Bytes	Addr.
			0000
Addr =			0001
0000			0002
	Addr =		0003
1	0000		0004
Addr =			0005
0004			0006
			0007
			8000
Addr =			0009
0008	Addr		0010
	=		0011
1	8000		0012
Addr =			0013
0012			0014
			0015
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Data Representations

Sizes of C Objects (in Bytes)

C Data Type Compaq	Alpha	Typical 32-bit	Intel IA32
• int	4	4	4
long int	8	4	4
• char	1	1	1
• short	2	2	2
float	4	4	4
double	8	8	8
long double	8	8	10/12
• char *	8	4	4

» Or any other pointer

Byte Ordering

How should bytes within multi-byte word be ordered in memory?

Conventions

- Sun's, Mac's are "Big Endian" machines
 - Least significant byte has highest address
- Alphas, PC's are "Little Endian" machines
 - Least significant byte has lowest address

Byte Ordering Example

Big Endian

Least significant byte has highest address

Little Endian

Least significant byte has lowest address

Example

- Variable x has 4-byte representation 0x01234567
- Address given by &x is 0x100

Big Endian			0x100	0x101	0x102	0x103	
			01	23	45	67	
Little Endia	0 x 100	0x101	0x102	0 x 103			
			67	45	23	01	

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Reading Byte-Reversed Listings

Disassembly

- Text representation of binary machine code
- Generated by program that reads the machine code

Example Fragment

Addr	ess Ins	struction Cod	le	Assembl	y Rendition
8048	365: 5b			pop	%ebx
8048	366: 81	c3 ab 12	00 00	add	\$0x12ab,%ebx
8048	36c: 83	bb 28 00	00 00 00	cmpl	0x0,0x28(%ebx)

Deciphering Numbers

■ Value: 0x12ab

■ Pad to 4 bytes: 0x000012ab

■ Split into bytes: 00 00 12 ab

■ Reverse: ab 12 00 00

Examining Data Representations

Code to Print Byte Representation of Data

■ Casting pointer to unsigned char * creates byte array

Printf directives:
%p: Print pointer
%x: Print Hexadecimal

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show bytes Execution Example

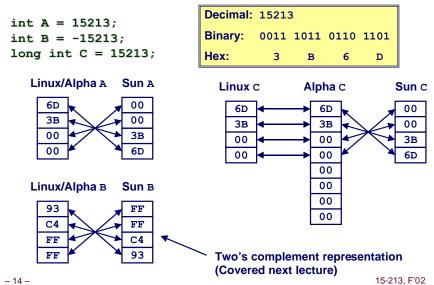
```
int a = 15213;
printf("int a = 15213;\n");
show_bytes((pointer) &a, sizeof(int));
```

Result (Linux):

```
int a = 15213;
0x11ffffcb8  0x6d
0x11ffffcb9  0x3b
0x11ffffcba  0x00
0x11ffffcbb  0x00
```

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Representing Integers



Representing Pointers

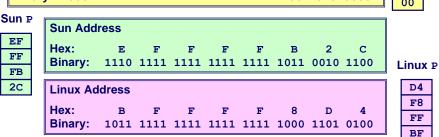
```
int B = -15213;

int *P = &B;

Alpha Address

Hex: 1 F F F F C A 0

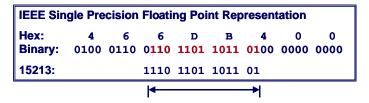
Binary: 0001 1111 1111 1111 1111 1110 1010 0000
```



Different compilers & machines assign different locations to objects

Representing Floats





Not same as integer representation, but consistent across machines Can see some relation to integer representation, but not obvious

Alpha P

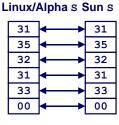
A0

Representing Strings

Strings in C

char S[6] = "15213";

- Represented by array of characters
- Each character encoded in ASCII format
 - Standard 7-bit encoding of character set
 - Other encodings exist, but uncommon
 - Character "0" has code 0x30
 - » Digit i has code 0x30+i
- String should be null-terminated
 - Final character = 0



Compatibility

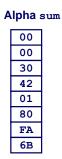
- Byte ordering not an issue
 - Data are single byte quantities
- Text files generally platform independent
 - Except for different conventions of line termination character(s)!
- (0).

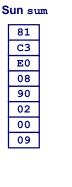
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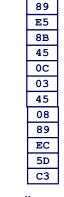
Representing Instructions

int sum(int x, int y)
{
 return x+y;
}

- For this example, Alpha & Sun use two 4-byte instructions
 - Use differing numbers of instructions in other cases
- PC uses 7 instructions with lengths 1, 2, and 3 bytes
 - Same for NT and for Linux
 - NT / Linux not fully binary compatible







PC sum

55

Different machines use totally different instructions and encodings

Machine-Level Code Representation

Encode Program as Sequence of Instructions

- Each simple operation
 - Arithmetic operation
 - Read or write memory
 - Conditional branch
- Instructions encoded as bytes
 - Alpha's, Sun's, Mac's use 4 byte instructions
 - » Reduced Instruction Set Computer (RISC)
 - PC's use variable length instructions
 - » Complex Instruction Set Computer (CISC)
- Different instruction types and encodings for different machines
 - Most code not binary compatible

Programs are Byte Sequences Too!

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Boolean Algebra

Developed by George Boole in 19th Century

- Algebraic representation of logic
 - Encode "True" as 1 and "False" as 0

And

■ A&B = 1 when both A=1 and

Not

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■ ~A = 1 when A=0



■ A|B = 1 when either A=1 or B=1

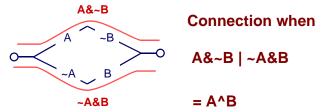
Exclusive-Or (Xor)

■ A^B = 1 when either A=1 or B=1, but not both

Application of Boolean Algebra

Applied to Digital Systems by Claude Shannon

- 1937 MIT Master's Thesis
- Reason about networks of relay switches
 - Encode closed switch as 1, open switch as 0



Integer Algebra

Integer Arithmetic

- ⟨Z, +, *, -, 0, 1⟩ forms a "ring"
- Addition is "sum" operation
- Multiplication is "product" operation
- - is additive inverse
- 0 is identity for sum
- 1 is identity for product

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Boolean Algebra

Boolean Algebra

- ({0,1}, |, &, ~, 0, 1) forms a "Boolean algebra"
- Or is "sum" operation
- And is "product" operation
- ~ is "complement" operation (not additive inverse)
- 0 is identity for sum
- 1 is identity for product

Boolean Algebra ≈ Integer Ring

■ Commutativity

Associativity

$$(A \mid B) \mid C = A \mid (B \mid C)$$
 $(A + B) + C = A + (B + C)$ $(A & B) & C = A & (B & C)$ $(A * B) * C = A * (B * C)$

■ Product distributes over sum

$$A & (B | C) = (A & B) | (A & C) A * (B + C) = A * B + B * C$$

Sum and product identities

$$A \mid 0 = A$$
 $A + 0 = A$ $A & 1 = A$ $A * 1 = A$

■ Zero is product annihilator

$$A \& 0 = 0$$
 $A * 0 = 0$

■ Cancellation of negation

$$\sim (\sim A) = A$$
 $-(-A) = A$

Boolean Algebra # Integer Ring

■ Boolean: Sum distributes over product

$$A \mid (B \& C) = (A \mid B) \& (A \mid C) \quad A + (B * C) \neq (A + B) * (B + C)$$

■ Boolean: *Idempotency*

$$A \mid A = A \qquad A + A \neq A$$

•"A is true" or "A is true" = "A is true"

$$A \& A = A \qquad A * A \neq A$$

■ Boolean: *Absorption*

$$A | (A \& B) = A$$
 $A + (A * B) \neq A$

•"A is true" or "A is true and B is true" = "A is true"

$$A & (A | B) = A$$
 $A * (A + B) \neq A$

■ Boolean: Laws of Complements

$$A \mid \neg A = 1 \qquad A + \neg A \neq 1$$

• "A is true" or "A is false"

Ring: Every element has additive inverse

$$A \mid \sim A \neq 0 \qquad \qquad A + -A = 0$$

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Boolean Ring

Properties of & and ^

- ⟨{0,1}, ^, &, *I*, 0, 1⟩
- Identical to integers mod 2
- I is identity operation: I(A) = A A ^ A = 0

Property

Boolean Ring

Commutative sum	$A ^B = B ^A$
Commutative product	A & B = B & A

■ Associative sum (A ^ B) ^ C = A ^ (B ^ C)
 ■ Associative product (A & B) & C = A & (B & C)
 ■ Prod. over sum A & (B ^ C) = (A & B) ^ (B & C)

0 is sum identity
 1 is prod. identity
 0 is product annihilator
 A & 1 = A
 0 & 0 = 0
 Additive inverse
 A ^ A = 0

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Relations Between Operations

DeMorgan's Laws

- Express & in terms of |, and vice-versa
 - A & B = ~(~A | ~B)
 - » A and B are true if and only if neither A nor B is false
 - A | B = ~(~A & ~B)
 - » A or B are true if and only if A and B are not both false

Exclusive-Or using Inclusive Or

- A ^ B = (~A & B) | (A & ~B)
 - » Exactly one of A and B is true
- A ^ B = (A | B) & ~(A & B)
 - » Either A is true, or B is true, but not both

General Boolean Algebras

Operate on Bit Vectors

■ Operations applied bitwise

All of the Properties of Boolean Algebra Apply

Representing & Manipulating Sets

Representation

- Width w bit vector represents subsets of {0, ..., w-1}
- $a_j = 1$ if $j \in A$ 01101001 {0, 3, 5, 6} 76543210 {0, 2, 4, 6}

Operations

&	Intersection	01000001 { 0, 6 }
• [Union	01111101 { 0, 2, 3, 4, 5, 6
^	Symmetric difference	00111100 { 2, 3, 4, 5 }
■ ~	Complement	10101010 { 1, 3, 5, 7 }

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Bit-Level Operations in C

Operations &, |, ~, ^ Available in C

- Apply to any "integral" data typelong, int, short, char
- View arguments as bit vectors
- Arguments applied bit-wise

Examples (Char data type)

- ~0x41 --> 0xBE ~01000001₂ --> 10111110₂
- ~0x00 --> 0xFF ~00000000₂ --> 11111111₂
- 0x69 & 0x55 --> 0x41 01101001₂ & 01010101₂ --> 01000001₂
- 0x69 | 0x55 --> 0x7D 01101001₂ | 01010101₂ --> 01111101₂

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Contrast: Logic Operations in C

Contrast to Logical Operators

- **&&**, ||,!
 - View 0 as "False"
 - Anything nonzero as "True"
 - Always return 0 or 1
 - Early termination

Examples (char data type)

- !0x41 --> 0x00
- !0x00 --> 0x01
- !!0x41 --> 0x01
- 0x69 && 0x55 --> 0x01
- 0x69 | | 0x55 --> 0x01
- p && *p (avoids null pointer access)

Shift Operations

Left Shift: x << y

- Shift bit-vector x left y positions
 - Throw away extra bits on left
 - Fill with 0's on right

Right Shift: $x \gg y$

- Shift bit-vector x right y positions
 - Throw away extra bits on right
- Logical shift
 - Fill with 0's on left
- Arithmetic shift
 - Replicate most significant bit on right
 - Useful with two's complement integer representation

Argument x	01100010
<< 3	00010 <i>000</i>
Log. >> 2	00011000
Arith. >> 2	00011000

Argument x	10100010
<< 3	00010 <i>000</i>
Log. >> 2	<i>00</i> 101000
Arith. >> 2	11101000

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Cool Stuff with Xor

- Bitwise Xor is form of addition
- With extra property that every value is its own additive inverse

$$A ^A A = 0$$

	*x	*y
Begin	A	В
1	A^B	В
2	A^B	$(A^B)^B = A$
3	$(A^B)^A = B$	A
End	В	A

Main Points

It's All About Bits & Bytes

- Numbers
- Programs
- Text

Different Machines Follow Different Conventions

- Word size
- Byte ordering
- Representations

Boolean Algebra is Mathematical Basis

- Basic form encodes "false" as 0, "true" as 1
- General form like bit-level operations in C
 - Good for representing & manipulating sets

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