Transactional Memory

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Applying Transactional Memory to Concurrency Bugs

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Transaction

A block of code declared to be an atomic region, executed by a single processor, and isolated from other regions

Atomicity \rightarrow execute to completion/not at all Consistency \rightarrow correctness (programmer's job) Isolation \rightarrow side-effects remain invisible until completion Durability \rightarrow not needed

Transactional Memory

An underlying memory system capable of execution transactions

H/W TM → speculative execution (with limitations) S/W TM → code instrumentation (performance issues) Hybrid TM → use H/W, fall back to S/W if necessary

Bug-Fix Techniques

Atomic regions \rightarrow execute atomically with isolation (speculative execution, locks)

 Explicit rollback → abort a partially executed transaction (opportunities for retries, mimic conditional variables)
Preemptive resources → transaction-friendly (revertible locks, I/O, system calls)

Atomic/lock serialization \rightarrow a bridge between atomic region and traditional locks

Concurrency Bugs

Writing correctly synchronized code can be challenge!

Deadlock (DL) \rightarrow use locks with a wrong order Atomicity Violation (AL) \rightarrow fail to protect critical sections

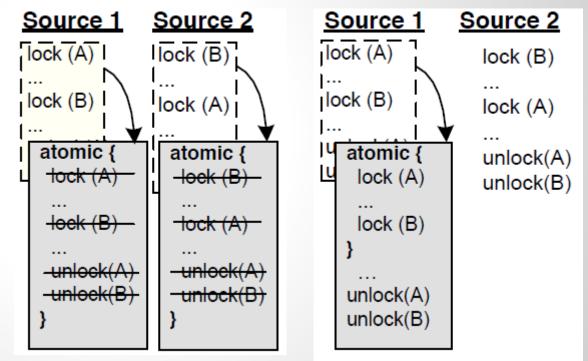
Resolving Deadlock Bugs

Recipe 1: Replacement of Deadlock-prone Locks

- (1) remove the use of multiple locks
- (2) automatically aborting conflicting threads
- (3) preserves concurrency

Recipe 2: Asymmetric Deadlock Preemption

- (1) retry lock acquisition if deadlock happens
- (2) require preemptive resources and deadlock detector
- (3) use TM only for atomicity, not isolation



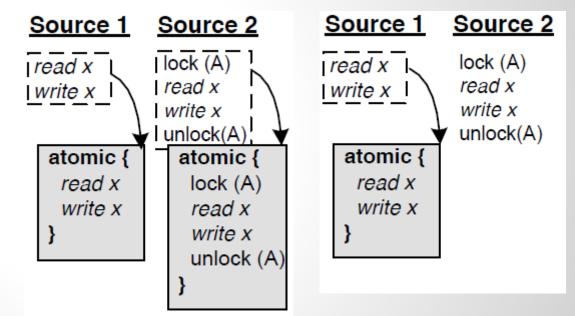
Addressing Atomic Violations

Recipe 1: Wrap All

- (1) use TM to achieve isolation and mutual exclusion
- (2) compatible with existing locks
- (3) no need to introduce new locks

Recipe 2: Warp Unprotected

- (1) reuse existing correct codes and keeps them unchanged
- (2) require atomic/lock serialization
- (3) may lead to performance issues



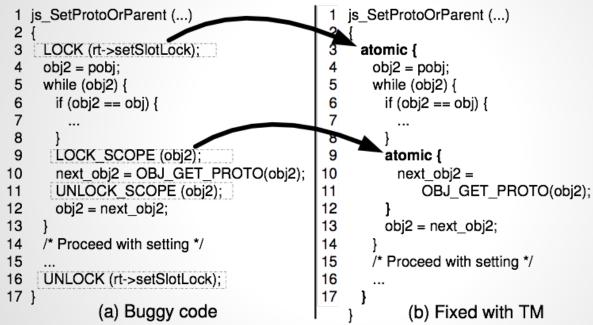
Evaluation & Case Study

Effectiveness

Bug	Арр	All	Transactional Memory Fixes				
			Total	R1	R2	R3	R4
DL	Mozilla	13	9	8(2)	-	7(1)	-
	Apache	4	2	1(1)	-	1(1)	-
	MySQL	5	1	0	-	1(1)	-
AV	Mozilla	25	20	-	20(12)	-	8(0)
	Apache	7	5	-	5(3)	-	2(0)
	MySQL	6	6	-	6(2)	-	4(0)
Total		60	43	9(2)	31(17)	9(3)	14(0)

- fix 43 out of 60 bugs: DL 12/22 AV31/38
- R1 and R2 can fix 40 out of 43

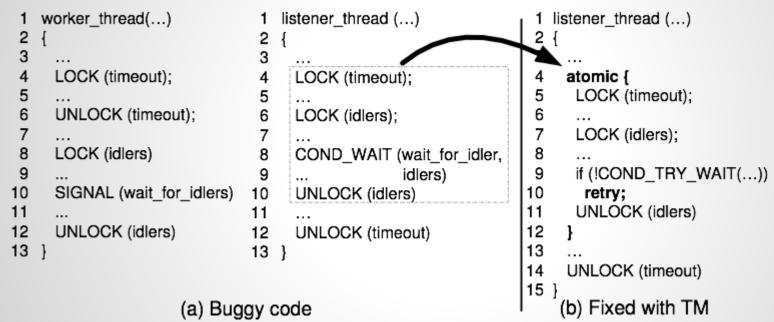
Mozilla-I: Deadlock



- Deadlock
 - two threads access the two locks in different order

- Mozilla-I: Deadlock
 - developer fix (hard)
 - force threads to drop ownership before blocking
 - new conditional variables
 - TM fixes
 - recipe 1: replace lock with atomic sections
 - recipe 3: revocable locks
 - Comparison
 - recipe1 solves fours other bugs (side effect)
 - performance: recipe 1: 79% worse

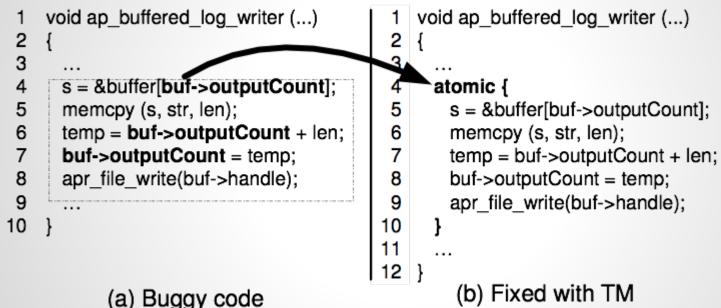
• Apache-I: deadlock



- Deadlock
 - listener first hold timeout then waiting for idle worker thread
 - worker thread first get timeout lock then signal

- Apache-I: deadlock
 - developer fix (hard)
 - release time out before wait
 - three failed attempts
 - TM fixes
 - recipe 3: abort transaction if wait
 - Comparison
 - simpler to fix
 - 28% worse in performance

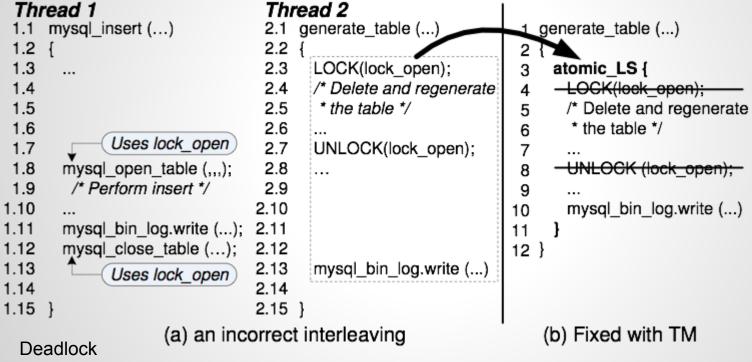
• Apache-II: missing synchronization



- race condition
 - two threads compete over the buffer

- Apache-II: missing synchronization
 - developer fix (medium difficulty)
 - use lock for each log device
 - TM fixes
 - insert single atomic block
 - easy
 - Comparison
 - TM simpler to fix
 - 4% slower

• MySQL-I: missing synchronization



• delete release lock too early

- Apache-II: missing synchronization
 - developer fix (hard)
 - extent lock_open to the end
 - performance implications
 - TM fixes
 - insert single atomic block
 - easy
 - Comparison
 - simple, expressive and non-invasive
 - same performance

Conclusion

- Very simple to use TM to fix bugs
- performance remains to be improved
- deal with cases can't tackle now