# Midterm Exam (Homework #6 / Homework #7)

Due Oct 15, 2022 at 11:59am Points 100 Questions 29 Available after Jan 13 at 10:59pm

Time Limit 160 Minutes

# Instructions

### Instructions

- · This exam is an individual effort.
- You are not permitted to help others, in any way, with this exam.
- You are not permitted to release or to discuss this exam with anyone, except the course staff, until given permission to do so by the instructors (which will not occur until all students have completed the exam. There may be exceptional cases that take it late).
- You are permitted to use only the official course textbook, the official course slides, and your own personal notes.
- A simple calculator is permitted, but won't prove to be helpful (we don't think).
- You have 160 minutes, from first exposure through submission to take this exam. Do not attempt to "peek", "check", or "test" the exam. This will start your clock.
- We only expect the exam to take 70-90 minutes.
- The exam counts for the 25% "exam portion" of the midterm grade, but is reduced to counting as a "double homework" for the final grade.
- In order to make the exam an "invested but low stakes" experience, half of this exam's weight toward the final grade may be dropped as one of the two "homework drops", but the full weight can't be dropped.

Question	Topic	Points
1	Integers	10
2	Floats	15
3	Array Sizes	5
4	Array Arithmetic	5
5	Structs and Alignment	12
6	Assembly: Basic	8
7	Assembly: Switch	15
8	Assembly: Loops and Conditionals	12
9	Memory Hierarchy	5
10	Locality	3

This quiz is no longer available as the course has been concluded.

# **Attempt History**

	Attempt	Time	Score
LATEST	Attempt 1	less than 1 minute	0 out of 100

Score for this quiz: 0 out of 100 Submitted Jan 13 at 10:59pm This attempt took less than 1 minute.

Unanswered

0 / 10 pts **Question 1** 

1. Integers (10 points, 2 points per blank)

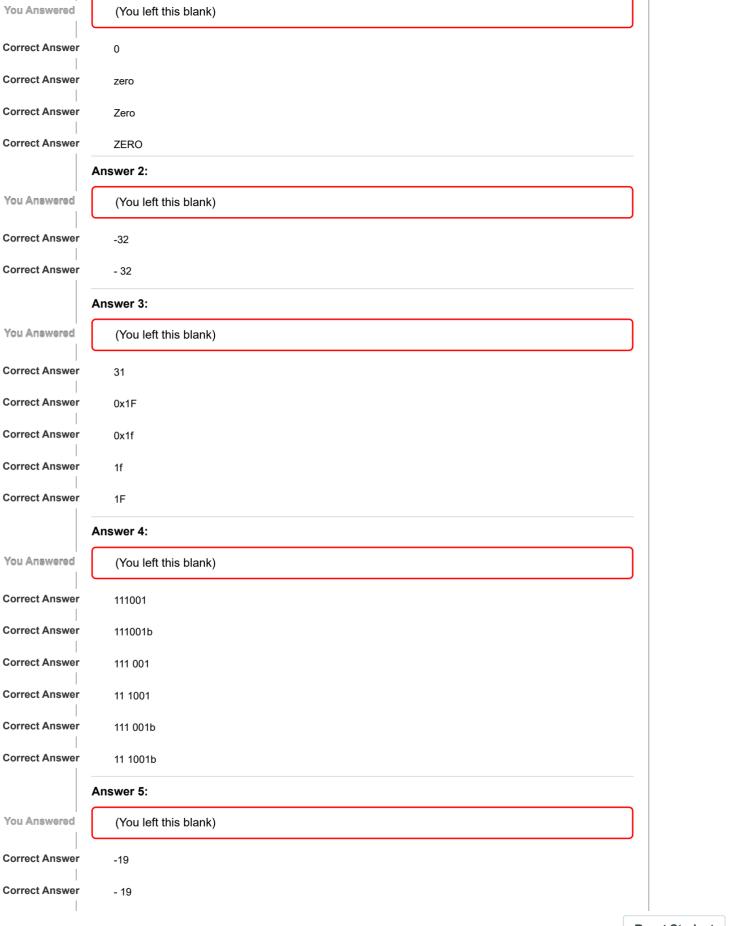
This question is based upon the following declaration on a machine using 6-bit two's complement arithmetic for signed integers.

Fill in the empty boxes in the table below.

- · Show all digits for the "Binary" column, including any leading 0s. Do not add spaces, letters, annotations, groupings, units, etc.
- You need not fill in entries marked with "--".
- TMax denotes the largest positive two's complement number
- TMin denotes the most negative two's complement number.

Expression	Decimal Representation	Binary Representation
0 + 0		-
Tmin		-
-28 - 5		-
	-7	
		101101

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### 2. Floats (15 points)

The floating point questions below are based upon an IEEE-like floating point format with the following specification:

- 9-bit width
- There is s = 1 sign bit
- There are k = 4 fraction bits
- Wherever rounding is necessary, round-to-even should be used. In addition, you should give the rounded value of the encoded floating point number.
- This question asks about the undecoded bits within the IEEE-like representation, answer in binary without spaces, groupings, annotations, letters, units, etc.

Unanswered	Question 2	0 / 1 pts
	Question 2: Floats (15 points, 1 point for this part)	
	2(A) (1 points) What is the bias? (Answer in decimal)	
You Answered		
Correct Answers	7 (with margin: 0)	
Unanswered	Question 3	0 / 1 pts
	Question 2: Floats (15 points, 1 point for this part)	
	2(B) (1 points) What is the exponent for denormalized numbers? (Answer in decimal)	
	Hint: This question asks about the actual, decoded exponent, not the bit pattern or value of the pattern in isolation.	e bit
You Answered		
Correct Answers	-6 (with margin: 0)	

	Question 2: Floats (15 points, 1 point for this part)
	2(C) (1 points) What is the maximum exponent for normalized numbers? (Answer in decimal)
	Hint: This question asks about the actual, decoded exponent, not the bit pattern or value of the bit pattern in isolation.
You Answered	
Correct Answers	7 (with margin: 0)
Unanswered	Question 5 0 / 1 pts
	Question 2: Floats (15 points, 1 point for this part)
	<b>2(D) (1 points)</b> What exponent bit pattern is used for special values (infinity, NaN, etc)? <i>Hint:</i> This question asks about the undecoded bits within the IEEE-like representation, answer in binary without spaces, groupings, annotations, letters, units, etc.
You Answered	
Correct Answers	1,111 (with margin: 0)
Unanswered	Question 6 0 / 11 pts

### Question 2: Floats (15 points, 1 point for each blank in this part)

This question is based upon an IEEE-like floating point format with the following specification:

- 9-bit width
- There is s = 1 sign bit
- There are k = 4 fraction bits
- Wherever rounding is necessary, round-to-even should be used. In addition, you should give the rounded value of the encoded floating point number.
- If the question asks about the undecoded bits within the IEEE-like representation, answer in binary without spaces, groupings, annotations, letters, units, etc.
- For the 3rd column: Answer as a fully reduced decimal fraction, i.e. use the smallest denominator possible without a fractional numerator.

2/E I) /4 naint nor blank) Fill in the following:

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	decimal fraction	decimal fraction
3/16		
-9/1024		
-Infinity		
19/2048	Fully reduced: + /	Fully reduced: (neglect sign)
17/2048	Fully reduced: + /	

## Answer 1: You Answered (You left this blank) **Correct Answer** 001001000 **Correct Answer** 0 0100 1000 **Correct Answer** 001001000b **Correct Answer** 001001000 b **Correct Answer** 0 0100 1000 **Correct Answer** 0 0100 1000 b Answer 2: You Answered (You left this blank)

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**Correct Answer** 1 0000 1001 **Correct Answer** 100001001b **Correct Answer** 1 0000 1001 b Answer 3: You Answered (You left this blank) **Correct Answer** 111110000 **Correct Answer** 111110000b **Correct Answer** 111110000 b **Correct Answer** 1 1111 0000 **Correct Answer** 1 1111 0000b **Correct Answer** 1 1111 0000 b Answer 4: You Answered (You left this blank) **Correct Answer** 000001010 **Correct Answer** 0 0000 1010 **Correct Answer** 000001010b **Correct Answer** 0 0000 1010b **Correct Answer** 000001010 b **Correct Answer** 0 0000 1010 b Answer 5: You Answered (You left this blank) **Correct Answer** 5 Answer 6: You Answered (You left this blank) **Correct Answer** 512 Answer 7: You Answered (You left this blank) **Correct Answer** 1

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Correct Answer	2048		
	Answer 9:		
You Answered	(You left this blank)		
Correct Answer	000001000		
Correct Answer	000001000Ь		
Correct Answer	000001000 b		
Correct Answer	0 000 01000		
Correct Answer	0 0000 1000ь		
Correct Answer	0 0000 1000 b		
	Answer 10:		
You Answered	(You left this blank)		
Correct Answer	1		
	Answer 11:		
You Answered	(You left this blank)		
Correct Answer	128		
Unanswered	Question 7		0 / 5 pts
	3. Arrays Sizes (5 points, 2.5pts p	er part)	
	Consider the following definitions in ints, and 8-byte longs. Answer with o	an x86-64 system with 8-byte pointers and 2-byte shorts, only a decimal number	4-byte
	Definition A	Definition B	
	int numbersA[ 5 ][ 3 ][ 2 ];	char *numbersB = numbersA;	
	<b>3(A) (2.5 points):</b> How many bytes a knowable): Bytes	are allocated to numbersA? (Write "UNKNOWN" if not	
	Hint: Think sizeof(); answer with on	nly a whole decimal number. No units. no fractions. No v	veirdness.
	Bytes		
You are cu		ng the test student will clear all history for dent, allowing you to view the course as a	Reset Student

	numbersB = numbersB	<del>+ 1,</del>	
	Hint: Answer with on	ly a whole decimal number. No units. no fractions. No weirdness.	
	В	ytes	
	Answer 1:		
u Answered	(You left this blan	k)	
rrect Answer	120		
rect Answer	120B		
rrect Answer	120 B		
rrect Answer	120bytes		
rrect Answer	120Bytes		
rrect Answer	120 Bytes		
rrect Answer	120 bytes		
	Answer 2:		
u Answered	(You left this blan	k)	
rect Answer	1		
rect Answer	1B		
rect Answer	1Bytes		
rrect Answer	1bytes		
rect Answer	1 B		
rrect Answer	1 Bytes		
rrect Answer	1 bytes		
nanswered	Question 8	0 / 5 pts	
	(2.5 points): Conside	5 points, 2.5 points per blank)  r the following definitions as implemented on a shark machine, i.e. x86-64 with 1-  orts, 4-byte ints, 8-byte longs, and 8-byte pointers.	
	Definition A	Definition B	

For each part below, write "UNKNOWN" (without quotes) if there is not enough information to answer the question or the answer is otherwise unknowable. Otherwise, answer with only a whole decimal number. No units. no fractions. No weirdness. 4(A) (2.5 points) What is the difference, i.e. number of bytes, between the addresses of numbersA[1][ 2 | and numbersA[ 2 ][ 1 ]? **Bytes** 4(B) (2.5 points) What is the difference, i.e. number of bytes, between numbersB[ 1 ][ 2 ] and numbersB[ 2 ][ 1 ]? **Bytes** Answer 1: You Answered (You left this blank) **Correct Answer** 16 **Correct Answer** 16B **Correct Answer** 16 B **Correct Answer** 16bytes **Correct Answer** 16 bytes **Correct Answer** 16 Bytes **Correct Answer** 16 Bytes Answer 2: You Answered (You left this blank) **Correct Answer** UNKNOWN **Correct Answer** Unknown **Correct Answer** unknown 5. Structs and Alignment (12 points, 2 points per part) The struct questions below are based upon the following definition as implemented on a shark machine, i.e. x86-64 with 1-byte chars, 2-byte shorts, 4-byte ints, 8-byte longs, and 8-byte pointers.

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	<pre>int i; } exam;</pre>	
	Assume a system which requires "natural alignment" (the alignment presented in lectures), i.e. each type needs to be aligned to a multiple of its data type size.	l
nswered	Question 9 0 / 2	pts
	<ul><li>5. Structs and Alignment (12 points, 2 points per part)</li><li>5(A) (2 points) What is the value of sizeof(struct exam)?</li></ul>	
Answered		
ect Answer	s 24 (with margin: 0)	
nswered	Question 10 0 / 2	pts
	<ul><li>5. Structs and Alignment (12 points, 2 points per part)</li><li>5(B) (2 points) How many bytes of padding does the compiler introduce after s?</li></ul>	
Answered		
ct Answer	s 4 (with margin: 0)	<u> </u>
answered	Question 11 0 / 2	pts
	<ul><li>5. Structs and Alignment (12 points, 2 points per part)</li><li>5(C) (2 points) How many bytes of padding does the compiler introduce after I?</li></ul>	
Answered		
rect Answer	s 0 (with margin: 0)	
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5. Structs and Alignment (12 points, 2 points per part)	
<b>5(D) (2 points)</b> How many bytes of padding does the compiler introduce after i?	
You Answered	
Correct Answers 4 (with margin: 0)	
Unanswered Question 13	0 / 2 pts
5. Structs and Alignment (10 points, 2 points per part)	
<b>5(E) (2 points)</b> Which of the following field orderings minimize the amount of padding introducompiler?	iced by the
○ s, c1, c2, l, i	
All of the above	
Correct Answer c1, c2, s, i, l	
○ c1, s, c2, i, l	
O None of the above	
○ c1, c2, s, l, i	
Unanswered Question 14	0 / 2 pts
5. Structs and Alignment (12 points, 2 points per part)	
<b>5(F) (2 points)</b> Assuming the fields of the struct were organized optimally by the programmer would be the value of sizeof (struct exam)?	r, what
You Answered	
Correct Answers 16 (with margin: 0)	

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### 7. Assembly-Switch (15 points)

Consider the following code, which was compiled from C Programming Language source code containing one switch statement and no (zero) if statements:

```
Dump of assembler code for function foo:
   0x00000000000400530 <+0>:
                                       $0x5,%esi
                               cmp
   0x00000000000400533 <+3>:
                                       0x400555 <foo+37>
                                ia
   0x0000000000400535 <+5>:
                                       %esi,%esi
                                mov
   0x00000000000400537 <+7>:
                                jmpq
                                       *0x400620(,%rsi,8)
   0x00000000000040053e <+14>:
                                       0x1(%rdi),%eax
                                lea
   0x00000000000400541 <+17>:
                                reta
   0x000000000000400542 <+18>:
                                       $0x2.%edi
                                sh1
   0x00000000000400545 <+21>:
                                       (%rdi,%rdi,1),%eax
                                lea
   0×00000000000400548 <+24>:
                                retq
   0x00000000000400549 <+25>:
                                       0x3(%rdi),%eax
                                lea
                                test
                                       %edi,%edi
   0x00000000000040054c <+28>:
   0x0000000000040054e <+30>:
                                cmovns %edi,%eax
   0x00000000000400551 <+33>:
                                sar
                                       $0x2,%eax
   0x000000000000400554 <+36>:
                                retq
                                       %edi,%eax
   0x00000000000400555 <+37>:
                                mov
   0x00000000000400557 <+39>:
                                shl
                                       $0x4,%eax
  0x0000000000040055a <+42>:
                                retq
End of assembler dump.
```

### Consider also the following dump:

```
(gdb) x/16gx 0x400610
0x400610:
                0x0000000000020001
                                        0x00000000000000000
0x400620:
                0x000000000040053e
                                        0x000000000040053e
0x400630:
                0x0000000000400542
                                        0x0000000000400545
0x400640:
                0x0000000000400555
                                        0x0000000000400549
0x400650:
                0x0000003c3b031b01
                                        0xfffffdb000000006
0x400660:
                0xfffffdf000000088
                                        0xfffffee000000058
0x400670:
                0xffffff0b000000b0
                                        0xffffff4000000c8
0x400680:
                0xffffffb0000000e8
                                        Cannot access memory at address 0x400688
```

# Unanswered Question 16 7. Assembly-Switch (15 points) 7(A) (3 points) Which of the following executes for case 3? Correct Answer lea (%rdi,%rdi,1),%eax None of the above shl \$0x4,%eax shl \$0x2,%edi

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	○ lea 0x1(%rdi),%eax
Unanswered	Question 17 0 / 3 pts
	<ul><li>7. Assembly-Switch (15 points)</li><li>7(B) (3 points) Which integer input values are managed by non-default cases of the switch statement? Check all that apply.</li></ul>
Correct Answer	<b>3</b>
Correct Answer	4 5
Correct Answer	
Correct Answer	2
	None of the above
Correct Answer	☐ Other value(s) in addition to those above
Unanswered	Question 18 0 / 3 pts
	<ul> <li>7. Assembly-Switch (15 points)</li> <li>7(C) (3 points) If there is a default case, at what address, in hex, does the begin?</li> <li>If there isn't a default case, write NONE.</li> <li>When writing an address, please do not include any leading 0s, prefixes or suffixes, or any spaces, and please write any letters in either all upper or all lower case, not mixed case.</li> <li>Your answer: [blank]</li> </ul>
You Answered	
Correct Answers	0x400555 400555 000000000400555 0x000000000400555

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Unanswered	Question 19 0	/ 3 pts
	7. Assembly-Switch (15 points)	
	<b>7(D) (3 points)</b> Which of the following case(s), if any, consist of exactly the same code as least of other case (no extra code, no code missing)? Check all that apply. [exact_same]	one
	5	
Correct Answer	_ o	
	3	
	□ None of the above	
Correct Answer	<b>1</b>	
Unanswered	Question 20 0	/ 3 pts
Unanswered	Question 20  7. Assembly-Switch (15 points)  7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their code?	
Unanswered	7. Assembly-Switch (15 points) 7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their of	
Unanswered	7. Assembly-Switch (15 points) 7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their code?	
Unanswered	7. Assembly-Switch (15 points)  7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their code?  None of the above	
Unanswered	7. Assembly-Switch (15 points) 7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their code?  None of the above	
Unanswered	7. Assembly-Switch (15 points)  7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their code?  None of the above  1 3	
	7. Assembly-Switch (15 points)  7(E) (3 points) Which case(s), if any, fall through to the next case after executing some of their code?  None of the above  1  3	

```
(gdb) disassemble loop
Dump of assembler code for function loop:
   0x0000000000040059d <+0>:
                                        %rbp
                                 nush
   0x0000000000040059e <+1>:
                                        %rsp,%rbp
                                 mov
   0x0000000000004005a1 <+4>:
                                        %r14
                                 push
   0x000000000004005a3 <+6>:
                                        %r13
                                 push
   0x000000000004005a5 <+8>:
                                        %r12
                                 nush
   0x0000000000004005a7 <+10>:
                                 push
                                        %rbx
   0x0000000000004005a8 <+11>:
                                        $0x20,%rsp
                                 sub
   0x0000000000004005ac <+15>:
                                        %edi,-0x34(%rbp)
                                 mov
   0x0000000000004005af <+18>:
                                 mov
                                        %esi.-0x38(%rbn)
                                        %edx,-0x3c(%rbp)
   0x0000000000004005h2 <+21>:
                                 mov
                                        $0x0,%r13d
   0x000000000004005b5 <+24>:
                                 mov
   0x0000000000000005hh <+30>
                                 mov
                                        $0x0,%r14d
   0x000000000004005c1 <+36>:
                                 mov
                                        $0x0,%ebx
   0x0000000000004005c6 <+41>:
                                 mov
                                        $0x0,%r12d
   0x0000000000004005cc <+47>:
                                 mov
                                        $0x64,%eax
   0x000000000004005d1 <+52>:
                                 cmpl
                                        $0x64,-0x34(%rbp)
   0x000000000004005d5 <+56>:
                                 cmovge -0x34(%rbp),%eax
                                 mov
                                       %eax,-0x34(%rbp)
   0x00000000004005d9 <+60>:
   0x000000000004005dc <+63>:
                                 mov
                                        $0x64,%eax
   0x000000000004005e1 <+68>:
                                 cmpl
                                        $0x64,-0x38(%rbp)
   0x00000000004005e5 <+72>:
                                 cmovge -0x38(%rbp),%eax
   0x00000000004005e9 <+76>:
                                        %eax,-0x38(%rbp)
   0x0000000000004005ec <+79>:
                                 mov
                                        $0x0,%ebx
   0x00000000004005f1 <+84>:
                                        0x40062c <loop+143>
   0x00000000004005f3 <+86>:
                                        $0x0,%r13d
   0x000000000004005f9 <+92>:
                                        $0x0,%r12d
   0x00000000004005ff <+98>:
                                        0x400620 <loop+131>
   0x0000000000400601 <+100>:
                                        -0x24(%rbp),%rax
   0x0000000000400605 <+104>:
                                 mov
                                        %rax,%rsi
   0x00000000000400608 <+107>:
                                        $0x400710,%edi
   0x000000000040060d <+112>:
                                 mov
                                        $0x0,%eax
   0x0000000000400612 <+117>:
                                 callq 0x4004a0 <__isoc99_scanf@plt>
   0x00000000000400617 <+122>:
                                        -0x24(%rbp),%eax
                                 add
   0x000000000040061a <+125>:
                                        %eax,%r13d
   0x0000000000040061d <+128>:
                                 add
   0x0000000000400620 <+131>:
                                        -0x38(%rbp),%r12d
                                 cmp
   0x00000000000400624 <+135>:
                                        0x400601 <loop+100>
   0x00000000000400626 <+137>:
                                 add
                                        %r13d,%r14d
   0x00000000000400629 <+140>:
                                        $0x1,%ebx
                                 add
                                        -0x34(%rbp),%ebx
   0x000000000040062c <+143>:
                                 cmp
   0x000000000040062f <+146>:
                                        0x4005f3 <loop+86>
   0x00000000000400631 <+148>:
                                        $0x63,%r14d
                                 cmp
   0x0000000000400635 <+152>:
                                        0x40063e <loop+161>
                                 ig
   0x00000000000400637 <+154>:
                                        $0x64,%eax
                                 mov
   0x0000000000040063c <+159>:
                                        0x400641 <loop+164>
                                 dmi
   0x0000000000040063e <+161>:
                                        %r14d,%eax
                                 mov
   0x000000000000400641 <+164>:
                                        $0x20,%rsp
                                 add
   0x00000000000400645 <+168>:
                                        %rbx
                                 pop
   0x000000000000400646 <+169>:
                                        %r12
                                 gog
   0x00000000000400648 <+171>:
                                 pop
                                        %r13
   0x0000000000040064a <+173>:
                                 non
                                        %r14
   0x00000000000040064c <+175>:
                                 pop
                                        %rbp
   0x0000000000040064d <+176>:
                                 reta
End of assembler dump.
```

# Unanswered Question 21 0 / 3 pts 8. Loops and Conditionals (12 points) 8(A) (3 points) How many loops are in the code?

orrect Answer	○ 2				
Unanswered	Question 22	0 / 3 pts			
	<ul><li>8. Loops and Conditionals (12 points)</li><li>8(B) (3 points) What is the relationship between/among the loop(s)?</li></ul>				
	One after another				
ect Answer	They are all nested				
	Nested and one after another				
	There is only one loop, so there is no relationship between or among loops				
nswered	Question 23	0 / 3 pts			
	8. Loops and Conditionals (12 points)				
	8(C) (3 points) Which of the following are true? Check all that apply.				
	Two or more loops have a stopping value in common, e.g. progress up to or down to the same num	ıber.			
	Two or more loops have a loop control variable in common				
ect Answer	The loop control variable (the variable used to test whether to loop again or exit the loop) of one loop is by or within another loop	used			
nanswered	Question 24	0 / 3 pts			
	8. Loops and Conditionals (12 points)				
	8(D) (3 points) How many times is the ?-operator likely used in the source C Language code?	1			
	O 3				

O 2			
O 4 or more			
Question 25 0 / 5 pt			
9. Memory Hierarchy (5 points)			
Your goal is to design a memory system with an average access time of 1.1nS or less.			
You are given the following:			
<ul> <li>L1 cache with an access time of 1ns and a hit rate of 99%</li> <li>L2 cache with an access time of 6ns</li> <li>Main memory with an access time of 106ns</li> </ul>			
The access times for L2 and Main memory are end-to-end times, i.e., the L2 time includes the time taken to check the L1 and the Main memory time includes the time taken to check the L1 and L2.			
What is the maximum permissible L2 cache miss rate, expressed as a percentage, e.g. 0 for 0% or 10 for 10%, or 12 for 12%, or 50 for 50%, or 92 for 92%. Please enter only a 1- or 2-digit number with <b>out</b> the % -sign. Do not enter fractions, values less than 1, etc. Round <b>up</b> to the nearest percent.			
0.05 (with margin: 0) 5 (with margin: 0)			
Question 26 0 / 3 pt			
10. Locality (3 points)			
Consider a cache with 8 sets, 2 lines/set, and a block size of 16 bytes on a system with 4-byte ints.			
What is the maximum stride (index step) size while sequentially accessing a 1D int array to maintain a cache miss rate of no more than 42%?			

• Total size: 64 bytes (not counting meta data) · Block offset bits: 2 · Replacement policy: Set-wise LRU · 8-bit addresses Unanswered 0 / 1 pts **Question 27** 11. Caching (10 points) 11(A) (1 point) How many lines per set? You Answered **Correct Answers** 2 (with margin: 0) Unanswered 0 / 1 pts **Question 28** 11. Caching (10 points) 11(B) (1 point) How many bytes per block? You Answered **Correct Answers** 4 (with margin: 0) Unanswered 0 / 8 pts **Question 29** 11. Caching (10 points) 11(C) (8 points, 0.5 points each blank): Consider the following memory access trace, which is in order and begins at the beginning of time. For each of the following memory accesses, please indicate if it hits or misses, and if it misses, if it suffers from a capacity miss, a conflict miss, or a cold miss: Hit or Miss? Miss Type?

**Question Number** Address Circle one (per row): Circle one (per row) [ Select 1 0xA2 Select 1

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11(C)(1)

• Number of sets: 8

11(C)(3)	0XD7	[ Select ]	[ Select ]
11(C)(4)	0X92	[ Select ]	[ Select ]
11(C)(5)	0XD3	[ Select ]	[ Select ]
11(C)(6)	0XB2	[ Select ]	[ Select ]
11(C)(7)	0XA1	[ Select ]	[ Select ]
11(C)(8)	0X92	[ Select ]	[ Select ]

# Answer 1: You Answered (You left this blank) **Correct Answer** Miss Answer 2: You Answered (You left this blank) **Correct Answer** Cold/Compulsory Answer 3: You Answered (You left this blank) **Correct Answer** Miss Answer 4: You Answered (You left this blank) **Correct Answer** Cold/Compulsory Answer 5: You Answered (You left this blank) **Correct Answer** Miss Answer 6: You Answered (You left this blank) **Correct Answer** Cold/Compulsory Answer 7:

You are currently logged into Student

Correct Answer	Miss	
	Answer 8:	
You Answered	(You left this blank)	
Correct Answer	Cold/Compulsory	
	Answer 9:	
You Answered	(You left this blank)	
Correct Answer	Hit	
	Answer 10:	
You Answered	(You left this blank)	
Correct Answer	N/A	
	Answer 11:	
You Answered	(You left this blank)	
Correct Answer	Miss	
	Answer 12:	
You Answered	(You left this blank)	
Correct Answer	Cold/Compulsory	
	Answer 13:	
You Answered	(You left this blank)	
Correct Answer	Hit	
	Answer 14:	
You Answered	(You left this blank)	
Correct Answer	N/A	
	Answer 15:	
You Answered	(You left this blank)	
Correct Answer	Miss	
	Answer 16:	
You Answered	(You left this blank)	
Correct Answer	Conflict	