

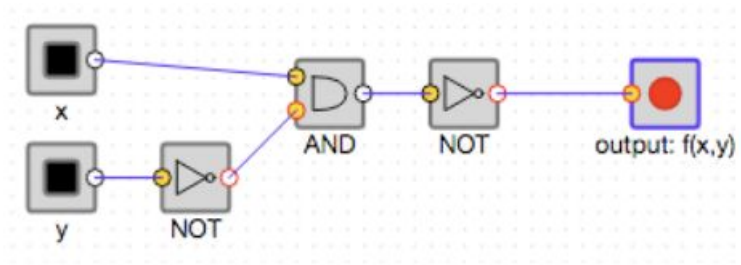
Circuits and Gates

1. Fill in the truth table for the NAND operation.

Hint: $\text{NAND} = \text{NOT} (\text{X AND Y})$

X	Y	X AND Y	X NAND Y

2. Given this circuit



Fill out the truth table:

x	y	f(x,y)
0	0	
0	1	
1	0	
1	1	