15-410 *"...misbehave(7)..."*

Project 2 Feb. 8, 2006

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- 1 - L11a_P2 15-410, S'06

Synchronization

P2 (et seq.) partners

- "Partner Registration Page" on web site
- Almost all people have already registered Thanks!
- If you know, please register today
 - This will help people still looking for partners

Please make sure you've discussed

- How many late days?
- Project schedule in other classes
 - Write down a joint project schedule
- Auditing or pass/fail? Target 410 grade?
- Prior experience
- Interviews

Outline

What you'll build

- Mutex, condition variable
- Thread library
- Supplemental library routines
- Tests

How the pieces fit together

- A picture is worth 1000 words
- You'll need to read the handouts too
 - (two, each >1000 words)
 - kspec specifies our kernel for P2, your kernel for P3
 - thr_lib specifies thread library

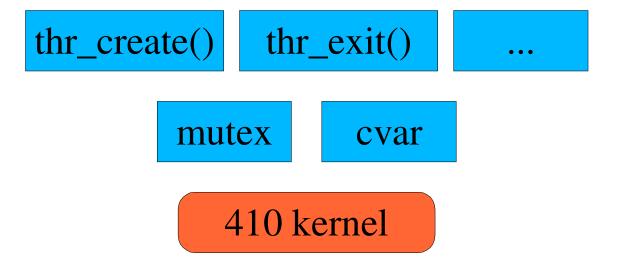
- 3 -

Mutex & Condition Variable

mutex cvar
410 kernel

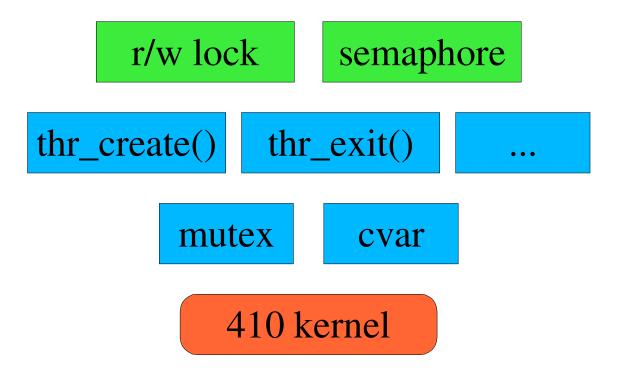
- **4** - 15-410, S'06

Remainder of Thread Library



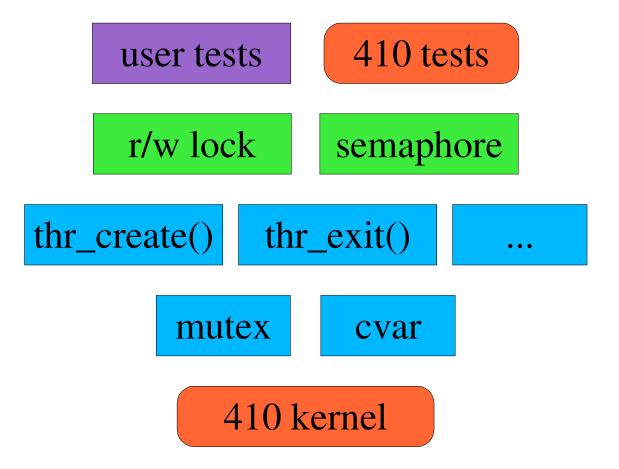
- 5 -

Supplemental Library Routines



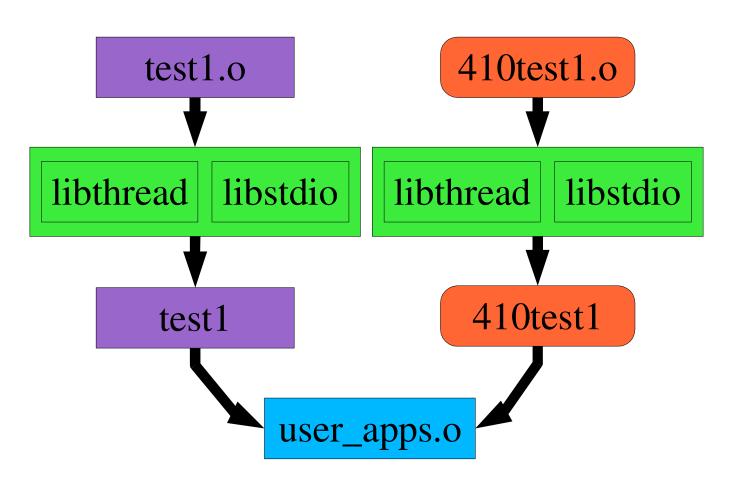
- 6 -

Tests (Yours & Ours)



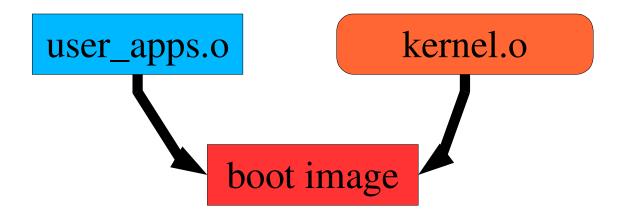
- 7 -

Building a "RAM disk" image



- 8 -

Linking "RAM disk" to kernel



- 9 -

Misbehave

misbehave(int mode)

- Special debugging-support system call in our 410 kernel
- Adjusts "behavior" of system
- Multiple legal behaviors (you will feel this during P3)
- Each mode selects a particular mix
 - We will not document these
 - We expect you to not "document" them to classmates either
- Debug your thread library with one mode, then the next...
- A dazzling array of flavors
 - 0...15
 - maybe even more
 - -1
- You will not be required to implement misbehave() in P3

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Plea – Conceptual

This code is *tricky*

- Most of you have already written multi-threaded code
 - That can be tricky enough
- Writing the internals is harder
 - Get a part 99% done
 - Discover a "bug"...
 - ...which is really a misconception...
 - Totally new design to fix it

Make sure core parts are solid

Better to skip readers/writers locks if not

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Plea – Time

The first 90% will take the first 90% of the time

The last 10% will take the second 90% of the time

"Code complete"

- Plan to spend at least three days debugging based on the tests we release
- If your thread library doesn't pass cyclone and agility_drill it won't pass a bunch of our tests either
 - Resultant grade is unlikely to exceed a C

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