

# Fault Tolerant NanoBoxes for Designing Computers Using Molecular Nanotechnology

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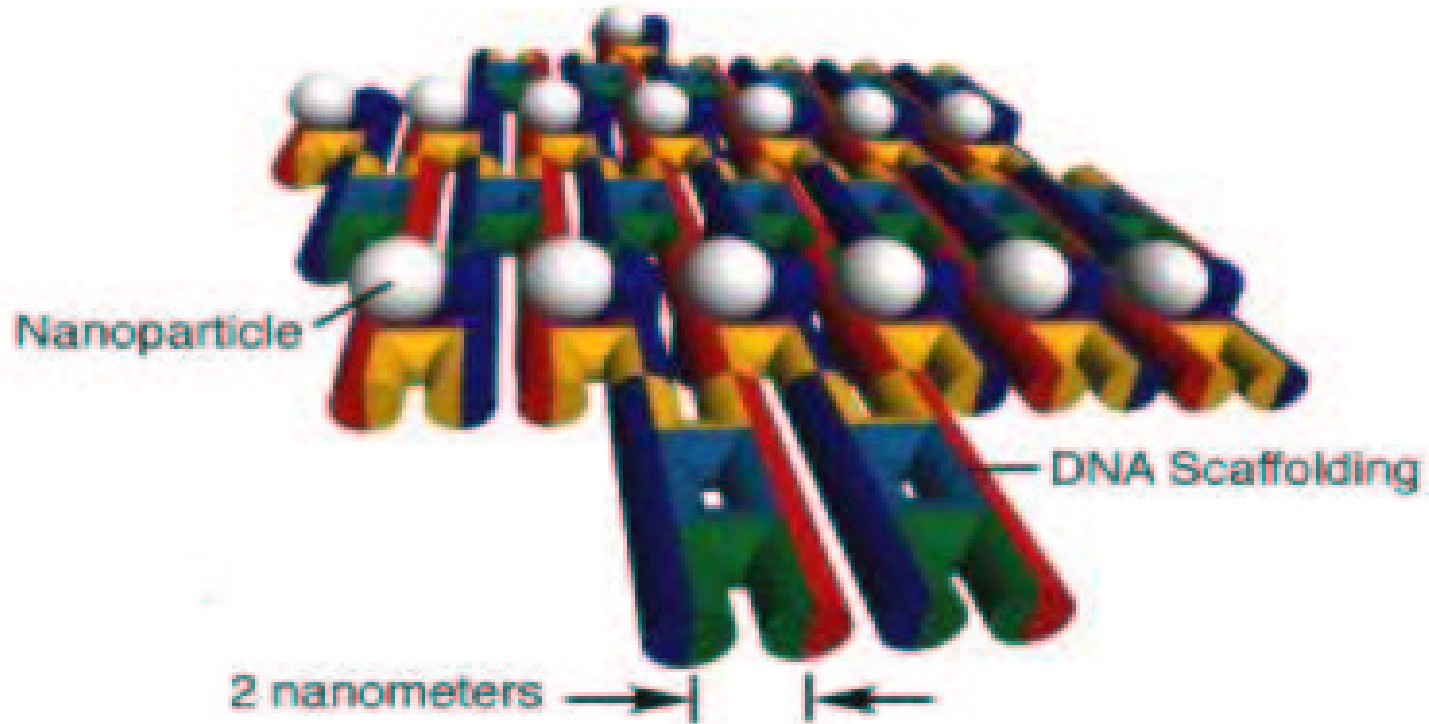
University of Minnesota

ARCTiC Labs [www.arctic.umn.edu](http://www.arctic.umn.edu)

# DNA Scaffold

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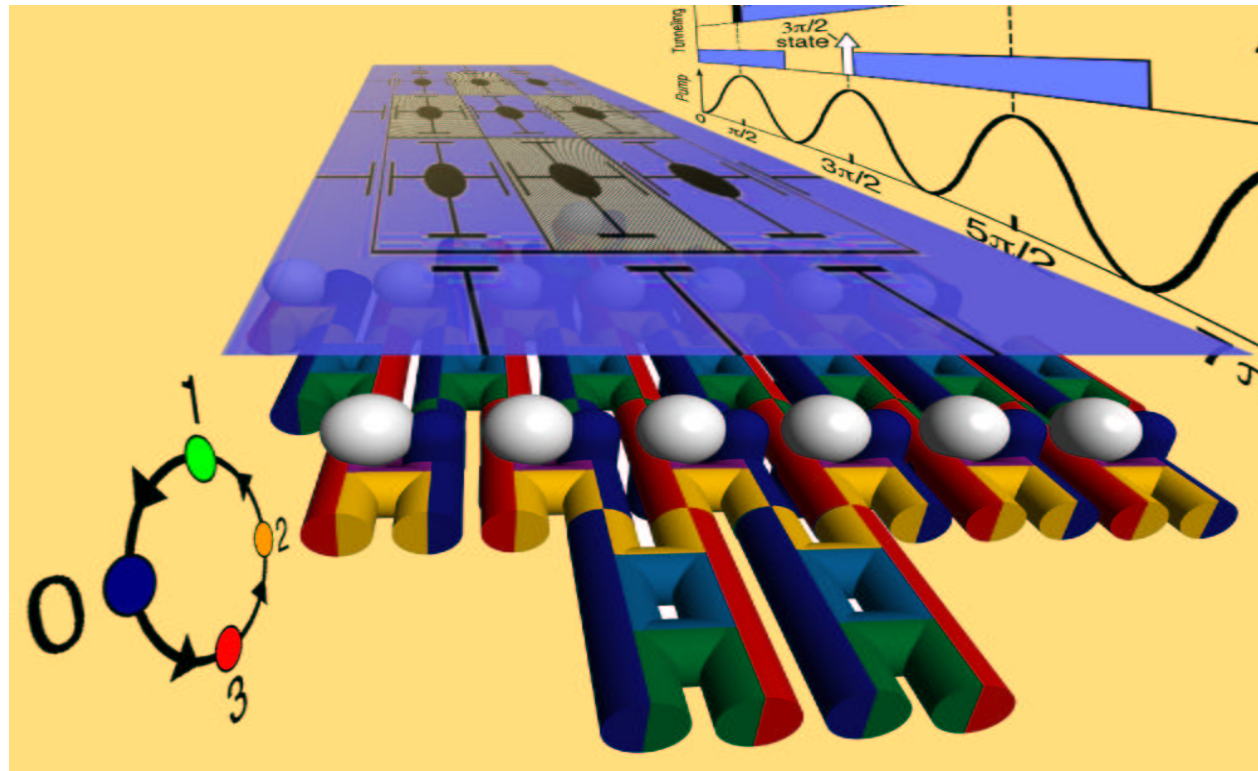


Proposed DNA Scaffold

# DNA Scaffold with Circuit Map

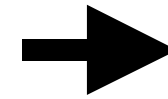
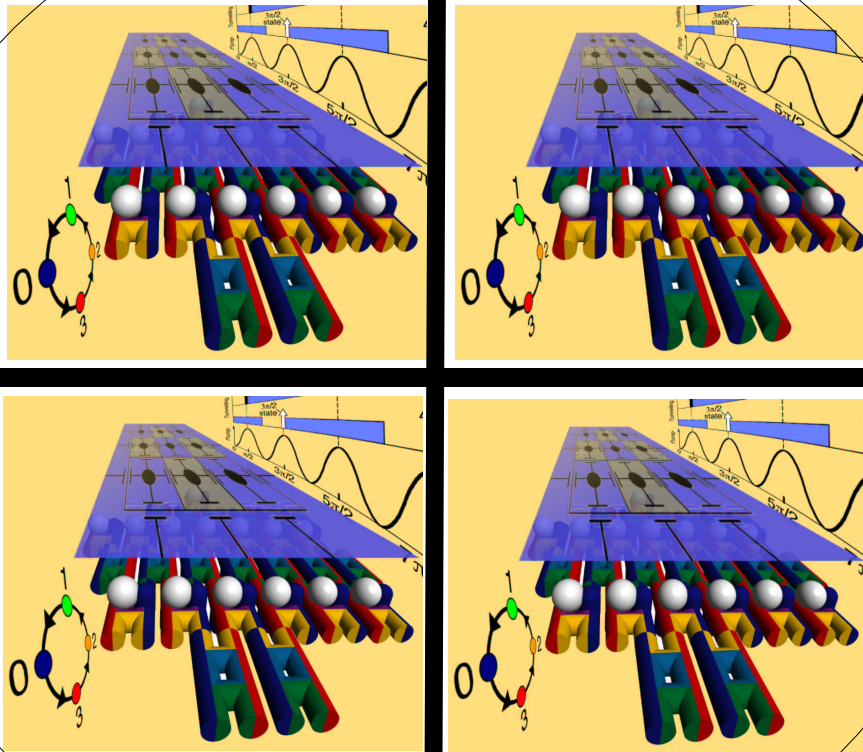
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Proposed DNA Scaffold with Circuit Mapping

# Logic Blocks From DNA Scaffold

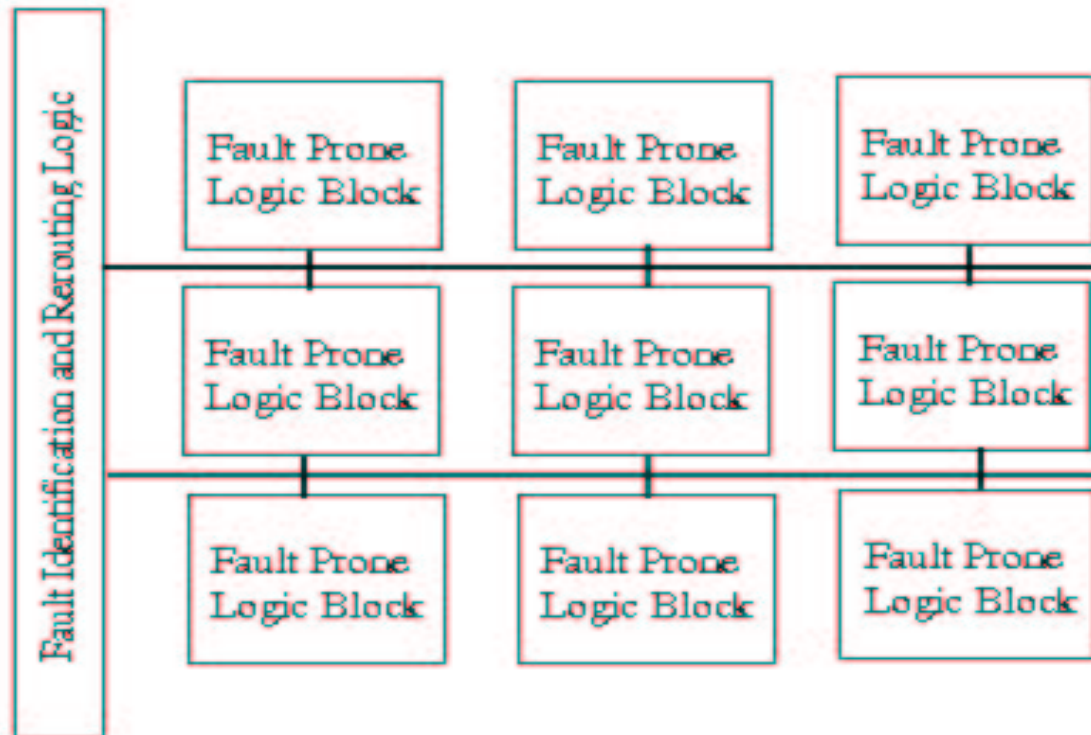


Becomes...

# FT Nano Architectures

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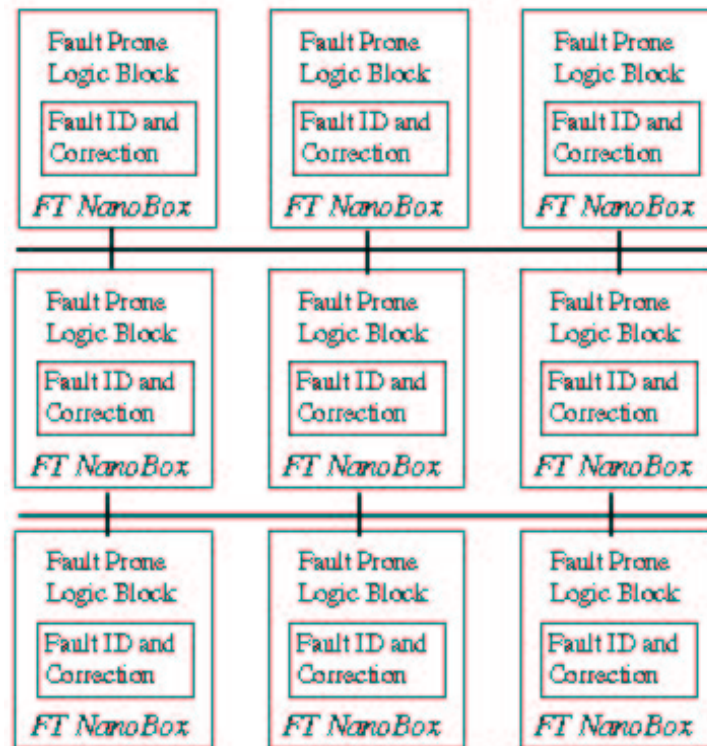


Approach to use external hardware to identify and route around faulty blocks

# FT Nano Architectures

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NanoBox approach incorporates fault tolerant techniques into black box components



# NanoBox Virtues

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- Abstraction makes circuit design easier
- NanoBox can be abstracted at varying levels of detail
- Fault identification and correction is deterministic



# NanoBox Vices

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- NanoBox interconnection errors not accounted for
- Keeping distributed FT overhead within reason will be challenging





# NanoBox Future Work

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- At what level to abstract?
- Start thinking about inter-NanoBox errors



# In Summary

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- Intro to our Molecular Technology  
DNA Scaffolding
- Progress to date with DNA Scaffolding  
Assembled rafts with gold clusters and attached  
rafts to mica
- Future architecture direction  
Encapsulated fault tolerance with self-healing  
NanoBoxes



# Questions and Discussion

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