# 15-414: Bug Catching: Automated Program Verification 

## Lecture Notes on Real-world SMT

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## 1 Introduction

In the previous lecture we studied the Nelson-Oppera procedure and the DPLL(T) framework that allows us to build decision procedures for formulas that use multiple theories. In this lecture, we will take a closer look to the theory of equality with uninterpreted functions and show how it can be used to abstract complex functions that may be hard to verify. As a real-world example, we will show how the theory of equality with uninterpreted functions can be used to prove equivalence of programs and how to use SMT solvers. Finally, we will present a decision procedure for the theory of equality with uninterpreted functions based on congruence closure. ${ }^{1}$

## 2 Preliminaries

We start by reviewing the signature and axioms of the theory of equality with uninterpreted functions.

$$
\Sigma_{\mathrm{E}}:\{=, a, b, c, \ldots, f, g, h, \ldots, p, q, r, \ldots\}
$$

consists of

- = (equality), a binary predicate;
- and all constant, function and predicate symbols.

The axioms of $T_{\mathrm{E}}$ are the following:

[^0]1. $\forall x \cdot x=x$
(reflexivity)
(symmetry)
(transitivity)
(congruence)
(equivalence)
2. $\forall \bar{x}, \bar{y} \cdot\left(\bigwedge_{i=1}^{n} x_{i}=y_{i}\right) \rightarrow(p(\bar{x}) \leftrightarrow p(\bar{y}))$

Consider the $\Sigma$-formula $\varphi$

$$
f(f(f(a)))=a \wedge f(f(f(f(f(a)))))=a \wedge f(a) \neq a
$$

$\varphi$ is $T_{\mathrm{E}}$-unsatisfiable. We can make the following intuitive argument: substituting $a$ for $f(f(f(a)))$ in $f(f(f(f(f(a)))))=a$ by the first equality yields $f(f(a))=a$; substituting $a$ for $f(f(a))$ in $f(f(f(a)))=a$ according to this new equality yields $f(a)=a$, contradicting the literal $f(a) \neq a$. More formally, we can apply the axioms of $T_{\mathrm{E}}$ and derive the same contradiction:

1. $f(f(f(f(a))))=f(a)$
first literal of $\varphi$ (congruence)
2. $f(f(f(f(f(f(a))))))=f(f(a))$ step 1 (congruence)
3. $f(f(a))=f(f(f(f(f(f(a))))))$
step 2 (symmetry)
4. $f(f(a))=a \quad$ step 3 and second literal of $\varphi$ (transitivity)

Note that even though we have the equivalence axiom, we can transform an instance of this axiom to an instance of the congruence axiom. This transformation allows us to disregard the equivalence axiom. For example, given $\Sigma$-formula:

$$
x=y \rightarrow(p(x) \leftrightarrow p(y))
$$

introduce a fresh constant $c$ and a fresh function $f_{p}$, and write

$$
\left.x=y \rightarrow\left(\left(f_{p}(x)=c\right)\right) \leftrightarrow\left(f_{p}(y)=c\right)\right)
$$

In the rest of this lecture, we will consider $\Sigma$-formulae without predicates other than $=$.

## 3 Proving equivalence of programs

Replacing functions with uninterpreted functions in a given formula is a common technique for making it easier to reason about (e.g., to prove its validity) At the same time, this process makes the formula weaker which means that it can make a valid formula invalid. This observation is summarized in the following relation, where $\varphi^{U F}$ is derived from a formula $\varphi$ by replacing some or all of its functions with uninterpreted functions:

```
int power3(int in)
{
    int i, out_a;
    out_a = in;
    for (i = 0; i < 2; i++)
        out_a = out_a * in;
    return out_a;
}
```

(a)
int power3_new (int in)
\{
int out_b;
out_b $=($ in $*$ in) $*$ in;
return out_b;
8 \}
(b)

Figure 1: Two C functions. We can simplify the proof of their equivalence by replacing the multiplication operator by an uninterpreted function.

$$
\vDash \varphi^{U F} \rightarrow \varphi
$$

Uninterpreted functions are widely used in calculus and other branches of mathematics, but in the context of reasoning and verification, they are mainly used for simplifying proofs. Under certain conditions, uninterpreted functions let us reason about systems while ignoring the semantics of all functions, assuming they are not necessary for the proof.

Assume that we have a method for checking the validity of a $\Sigma$-formula in $T_{\mathrm{E}} .{ }^{2}$ Relying on this assumption, the basic scheme for using uninterpreted functions is the following:

1. Let $\varphi$ denote a formula of interest that has interpreted functions. Assume that a validity check of $\varphi$ is too hard (computationally), or even impossible.
2. Assign an uninterpreted function to each interpreted function in $\varphi$. Substitute each function in $\varphi$ with the uninterpreted function to which it is mapped. Denote the new formula by $\varphi^{U F}$.
3. Check the validity of $\varphi^{U F}$. If it is valid then $\varphi$ is valid. Otherwise, we do not know anything about the validity of $\varphi$.

As a motivating example consider the problem of proving the equivalence of two C functions shown in Figure 1. In general, proving the equivalence of two programs is undecidable, which means there is no sound and complete to prove such an equivalence. However, in this case equivalence can be decided since the program does not have unbounded memory usage. A key observation about these programs is that they have only bounded loops, and therefore it is possible to compute their input/output relations. The derivation of these relations from these two programs can be as follows:

1. Remove the variable declarations and "return statements".
[^1]```
out0_ \(a=\) in \(0 \_a \wedge\)
out1_ \(a=\) out \(0 \_a * i n 0 \_a \wedge \quad\) out0_ \(b=\left(i n 0 \_b * i n 0 \_b\right) * i n 0 \_b\)
out2_ \(a=o u t 1 \_a * i n 0 \_a\)
```

(a) $\left(\varphi_{a}\right)$
(b) $\left(\varphi_{a}\right)$

Figure 2: Two formulas corresponding to the programs (a) and (b) in Figure 1.
2. Unroll the for loop.
3. Replace the left-hand side variable in each assignment with a new auxiliary variable.
4. Whenever a variable is read, replace it with the auxiliary variable that replaced it in the last place where it was assigned.
5. Conjoin all program statements.

These operations result in the two formulas $\varphi_{a}$ and $\varphi_{b}$ which are shown in Figure 2. This procedure to transform code into a first-order formula is known as static single assignment (SSA). A generalization of this form to programs with "if" branches and other constructs will be further explored in future lectures when discussing bounded model checking. For this lecture, we apply a limited form of SSA to illustrate how uninterpreted functions can be used to abstract the multiplication operator.
To show that these programs are equivalent with respect to their input-outputs, we must show that the following formula $\Phi$ is valid:

$$
i n 0 \_a=i n 0 \_b \wedge \varphi_{a} \wedge \varphi_{b} \rightarrow o u t 2 \_a=\text { out0_ } b
$$

Showing the validity of $\Phi$ is equivalent to show the unsatisfiability of $\neg \Phi$. We can show that $\neg \Phi$ is unsatisfiable by using SMT solvers.

## 4 Using SMT solvers

SMT solvers take as input a formula in a standardize format (SMT2-Lib format). A detailed description of the SMT2-Lib format is available at:

```
http://smtlib.cs.uiowa.edu
```

SMT solvers support a variety of theories, namely: the theory of arrays with extensionality, the theory of bit vectors with arbitrary size, the core theory defining the basic Boolean operators, the theory of floating point numbers, the theory of integer number and the theory of reals. ${ }^{3}$

[^2]```
(declare-fun out0_a () (Int))
(declare-fun out1_a () (Int))
(declare-fun inO_a () (Int))
(declare-fun out2_a () (Int))
(declare-fun out0_b () (Int))
(declare-fun inO_b () (Int))
(define-fun phi_a () Bool
    (and (= outO_a inO_a) ; out0_a = inO_a
            (and (= out1_a (* out0_a in0_a)) ; out1_a = out0_a * inO_a
                    (= out2_a (* out1_a in0_a))))) ; out2_a = out1_a * in0_a
(define-fun phi_b () Bool
    (= out0_b (* (* in0_b inO_b) inO_b))) ; out0_b = inO_b * inO_b *
            in0_b
(define-fun phi_input () Bool
    (= in0_a inO_b))
(define-fun phi_output () Bool
    (= out2_a out0_b))
(assert (not (=> (and phi_input phi_a phi_b) phi_output)))
(check-sat)
```

Figure 3: SMT encoding of $\Phi$ using mathematical integers to model integers.

If you want to try SMT solving, we recommend doing the z3 tutorial at:
https://rise4fun.com/z3/tutorial
and trying $z 3$ online at:

```
https://rise4fun.com/z3/
```

Before using SMT solvers to show that $\neg \Phi$ is unsatisfiable, we must decide how we will model integers since this will restrict the underlying theories used by the SMT solver.

### 4.1 Modeling integers as mathematical integers

If we model integers as mathematical integer than the SMT solver will use the theory of integers and will be able to show that both programs are equivalent. Figure 3 shows the SMT encoding of $\Phi$ when using integers: You can try this encoding online at:

```
https://rise4fun.com/Z3/BLQpl
```

However, integers are not represented as mathematical integers in C. If we want to model integers as the ones being used in $C$ then we should model them using bit vectors (of size 32 or 64 ).

```
(declare-fun out0_a () (_ BitVec 512))
(declare-fun out1_a () (_ BitVec 512))
(declare-fun inO_a () (_ BitVec 512))
(declare-fun out2_a () (_ BitVec 512))
(declare-fun out0_b () (_ BitVec 512))
(declare-fun inO_b () (_ BitVec 512))
(define-fun phi_a () Bool
    (and (= outO_a inO_a) ; outO_a = inO_a
            (and (= out1_a (bvmul out0_a inO_a)) ; out1_a = out0_a * in0_a
                    (= out2_a (bvmul out1_a in0_a))))) ; out2_a = out1_a * in0_a
(define-fun phi_b () Bool
    (= out0_b (bvmul (bvmul inO_b inO_b) inO_b))) ; out0_b = inO_b *
            in0_b * inO_b
(define-fun phi_input () Bool
    (= inO_a inO_b))
(define-fun phi_output () Bool
    (= out2_a out0_b))
(assert (not (=> (and phi_input phi_a phi_b) phi_output)))
(check-sat)
```

Figure 4: SMT encoding of $\Phi$ using bit vectors to model integers.

### 4.2 Modeling integers as bit vectors

Modeling integers as bit vectors as the advantage of capturing the $C$ model and being able to detect potential overflows. However, using the bit vector theory is not as efficient as using the theory of integers. In particular, assume we want to show that the programs are equivalent for a bit width of 512. The SMT encoding when using bit vectors is shown in the Figure 5. You can try this encoding online at:
https://rise4fun.com/Z3/ibsw3

This formula is much more challenging to be solved than the previous one and will become harder as the bit with increases. For example, if you try it online you will get an out of memory error. You can also try it in your own computer (since you should have $z 3$ installed) by running the following command:
\$ z3 -smt2 formula
where formula is a file with the contents of Figure 5. The reason for the memory blowup is the multiplication operator when using bit vectors. Can we avoid this issue altogether? What if we consider the multiplication operator as an uninterpreted function?

### 4.3 Using uninterpreted functions

If we consider an uninterpreted function $f$ that takes as input two bit vectors and returns a bit vector than we can replace the bit vector multiplication operator (bvmul) by $f$. If we are able to prove that this formula is unsatisfiable, then we can conclude that

```
(declare-fun outO_a () (_ BitVec 512))
(declare-fun out1_a () (_ BitVec 512))
(declare-fun in0_a () (_ BitVec 512))
(declare-fun out2_a () (_ BitVec 512))
(declare-fun outO_b () (_ BitVec 512))
(declare-fun inO_b () (_ BitVec 512))
(declare-fun f ((_ BitVec 512) (_ BitVec 512)) (_ BitVec 512))
(define-fun phi_a () Bool
    (and (= out0_a inO_a) ; out0_a = inO_a
            (and (= out1_a (f out0_a inO_a)) ; out1_a = out0_a * inO_a
                    (= out2_a (f out1_a in0_a))))) ; out2_a = out1_a * inO_a
(define-fun phi_b () Bool
    (= out0_b (f (f in0_b in0_b) in0_b))) ; out0_b = in0_b * in0_b *
        in0_b
(define-fun phi_input () Bool
    (= inO_a in0_b))
(define-fun phi_output () Bool
    (= out2_a out0_b))
(assert (not (=> (and phi_input phi_a phi_b) phi_output)))
(check-sat)
```

Figure 5: SMT encoding of $\Phi$ using an uninterpreted function for multiplication.
the original formula is also unsatisfiable and we are able to show the equivalence between the two programs when representing integers by bit vectors of width 512. This formula is much easier to be solved than the one using bit vector multiplication since we abstracted the multiplication function and the SMT solver will not need to reason about what $f$ does but only that it is a function. You can try this encoding online at:
https://rise4fun.com/Z3/V7Sf
But how can we show the satisfiability of a formula with equality and uninterpreted functions? In the next section, we will show a procedure based on congruence closure to determine the satisfiability of a formula with equality and uninterpreted functions.

## 5 Congruence closure

Each positive positive literal $s=t$ of a $\Sigma$-formula $\varphi$ over $T_{\mathrm{E}}$ asserts an equality between two terms $s$ and $t$. Applying the axioms of $T_{\mathrm{E}}$ produces more equalities over terms that occur in $\varphi$. Since there are only a finite number of terms in $\varphi$, only a finite number of equalities among these terms are possible. Hence, one of two situations eventually occurs: either some equality is formed that directly contradicts a negative literal $s^{\prime} \neq$ $t^{\prime}$ of $\varphi$; or the propagation of equalities ends without finding a contradiction. These cases correspond to $T_{\mathrm{E}}$-unsatisfiability and $T_{\mathrm{E}}$-satisfiability, respectively, of $\varphi$. In this section, we will formally describe this procedure as forming the congruence closure of the equality relation over terms asserted by $\varphi$.

Definition 1 (Equivalence relation). A binary relation $R$ over a set $S$ is an equivalence relation if:

1. Reflexive: $\forall_{s} \in S . s R s$;
2. Symmetric: $\forall_{s_{1}, s_{2}} \in S . s_{1} R s_{2} \rightarrow s_{2} R s_{1}$;
3. Transitive: $\forall_{s_{1}, s_{2}, s_{3}} \in S . s_{1} R s_{2} \wedge s_{2} R s_{3} \rightarrow s_{1} R s_{3}$.

For example, the relation $=$ is an equivalence relation over real numbers and $\equiv_{2}$ is an equivalence relation over $\mathbb{Z}$.

Definition 2 (Congruence relation). Consider a set $S$ equipped with functions $F=$ $\left\{f_{1}, \ldots, f_{n}\right\}$. A relation $R$ over $S$ is a congruence relation if it is an equivalence relation and for every $n$-ary function $f \in F$ :

$$
\forall_{\bar{s}, \bar{t}} \bigwedge_{i=1}^{n} s_{i} R t_{i} \rightarrow f(\bar{s}) R f(\bar{t})
$$

Definition 3 (Equivalence and congruence classes). For a given equivalence relation over $S$, every member of $S$ belongs to an equivalence class. The equivalence class of $s \in S$ under R is the set:

$$
[s]_{R} \stackrel{\text { def }}{=}\left\{s^{\prime} \in S: s R s^{\prime}\right\}
$$

If $R$ is a congruence relation then this set is called a congruence class.
For example, the equivalence class of 1 under $\equiv_{2}$ are the odd numbers and the equivalence class f 6 under $\equiv_{3}$ the multiples of 3 .

Definition 4 (Equivalence closure). The equivalence closure $R^{E}$ of the binary relation $R$ over $S$ is the equivalence relation such that:

- $R \subseteq R^{E}$;
- for all other equivalence relations $R^{\prime}$ s.t. $R \subseteq R^{\prime}, R^{E} \subseteq R^{\prime}$.

Thus, $R^{E}$ is the smallest equivalence relation that includes $R$.
Let $S=\{a, b, c, d\}$ and $R=\{a R b, b R c, d R d\}$ then

- $a R b, b R c, d R d \in R^{E}$
since $R \subseteq R^{E}$;
- $a R a, b R b, c R c \in R^{E}$ by reflexivity;
- $b R a, c R b \in R^{E}$
by symmetry;
- $a R c \in R^{E}$ by transitivity;
- $c R a \in R^{E}$
by symmetry;

Hence,

$$
R^{E}=\{a R b, b R a, a R z, b R b, b R c, c R b, c R c, a R c, c R a, d R d\}
$$

Definition 5 (Subterm set). The subterm set $S_{\varphi}$ of $\Sigma$-formula $\varphi$ is the set that contains precisely the subterms of $\varphi$.

For example, the subterm set of $\varphi$ :

$$
\varphi: f(a, b)=a \wedge f(f(a, b), b) \neq a
$$

is

$$
S_{\varphi}=\{a, b, f(a, b), f(f(a, b), b)\}
$$

Now we relate the congruence closure of a $\Sigma$-formula's subterm set with its $T_{\mathrm{E}}-$ satisfiability. Given $\Sigma$-formula $\varphi$

$$
\varphi: s_{1}=t_{1} \wedge \ldots \wedge s_{m}=t_{m} \wedge s_{m+1} \neq t_{m+1} \wedge \ldots \wedge s_{n} \neq t_{n}
$$

with subterm set $S_{\varphi}, \varphi$ is $T_{\mathrm{E}}$-satisfiable iff there exists a congruence relation $\sim$ over $S_{\varphi}$ such that

- for each $i \in\{1, \ldots, m\}, s_{i} \sim t_{i}$;
- for each $i \in\{m+1, \ldots, n\}, s_{i} \nsim t_{i}$.

The goal of the congruence closure algorithm is to construct the congruence relation of a formula's subterm set, or to prove that no congruence relation exists. The algorithm performs the following steps:

1. Construct the congruence closure $\sim$ of

$$
\left\{s_{1}=t_{1}, \ldots, s_{m}=t_{m}\right\}
$$

over the subterm set $S_{\varphi}$. Then

$$
\sim \models s_{1}=t_{1} \wedge \ldots \wedge s_{m}=t_{m}
$$

2. If $s_{i} \sim t_{i}$ for any $i \in\{m+1, \ldots, n\}$ then $\varphi$ is unsatisfiable;
3. Otherwise, $\sim \models \varphi$ and $\varphi$ is satisfiable.

How do we actually construct the congruence closure in Step 1? Initially, begin with the finest congruence relation $\sim_{0}$ given by the partition

$$
\left\{\{s\}: s \in S_{\varphi}\right\}
$$

in which each term of $S_{\varphi}$ is its own congruence class. Then, for each $i \in\{1, \ldots, m\}$, impose $s_{i}=t_{i}$ by merging the congruence classes

$$
\left[s_{i}\right] \sim_{i-1} \text { and }\left[t_{i}\right] \sim_{i-1}
$$

to form a new congruence relation $\sim_{i}$. To accomplish this merging, first form the union of $\left[s_{i}\right] \sim_{i-1}$ and $\left[t_{i}\right] \sim_{i-1}$. Then propagate any new congruence that arise within this union.

Consider the $\Sigma$-formula $\varphi$

$$
\varphi: f(a, b)=a \wedge f(f(a, b), b) \neq a
$$

Construct the following initial partition by letting each member of the subterm set $S_{\varphi}$ be its own class:

$$
\{\{a\},\{b\},\{f(a, b)\},\{f(f(a, b), b)\}\}
$$

According to the first literal $f(a, b)=a$, merge

$$
\{f(a, b),\{a\}\}
$$

to form partition

$$
\{\{a, f(a, b)\},\{b\},\{f(f(a, b), b)\}\}
$$

According to the congruence axiom,

$$
f(a, b) \sim a, b \sim b \text { implies } f(f(a, b), b) \sim f(a, b)
$$

resulting in the new partition

$$
\{\{a, f(a, b), f(f(a, b), b)\},\{b\}\}
$$

This partition represents the congruence closure of $S_{\varphi}$. Now, it is the case that

$$
\{\{a, f(a, b), f(f(a, b), b)\},\{b\}\} \vDash \varphi ?
$$

No! Since $f(f(a, b), b) \sim a$ but $\varphi$ asserts that $f(f(a, b), b) \neq a$. Therefore, $\varphi$ is $T_{\mathrm{E}^{-}}$ unsatisfiable.

## 6 Summary

- Uninterpreted functions can be used to simplify proofs by replacing (complex) interpreted functions by uninterpreted functions;
- Let $\varphi^{U F}$ be $\varphi$ with all its interpreted functions replaced by uninterpreted functions. Then:

$$
\models \varphi^{U F} \rightarrow \varphi
$$

- We can use SMT solvers to check if two programs are equivalent:
- If we represent integers as mathematical integers than the problem is relatively easy to be solved;
- If we represent integers as bit vectors than the problem becomes more challenging because of bit vector multiplication;
- If we abstract bit vector multiplication with uninterpreted functions than we can achieve scalability and prove that two programs are equivalent for any bit width.
- Congruence closure can be use to check the satisfiability of a formula with equality and uninterpreted functions.


## References

[BM07] Aaron R. Bradley and Zohar Manna. The Calculus of Computation: Decision Procedures with Applications to Verification. Springer-Verlag New York, Inc., Secaucus, NJ, USA, 2007.
[KS16] Daniel Kroening and Ofer Strichman. Decision Procedures - An Algorithmic Point of View. Texts in Theoretical Computer Science. An EATCS Series. Springer, 2016.


[^0]:    ${ }^{1}$ Lecture notes based on [BM07] and [KS16].

[^1]:    ${ }^{2}$ Later in this lecture we will show how to check the validity of a $\Sigma$-formula in $T_{\mathrm{E}}$ with congruence closure.

[^2]:    ${ }^{3}$ Further details on each theory are available at http://smtlib.cs.uiowa.edu/theories.shtml.

