#### **19 January**

## Welcome to 15-411 Recitation!

In recitation, we will both review what was discussed in lecture and also give you practical tips, tricks, and advice that will help you when implementing your compilers. Please participate and ask questions! We're here to help you succeed.

#### **Course Infrastructure**

We'll be giving each of you access to two GitHub repositories:

- dist: Starter code, test cases, and tools (read-only)
- <Your team name>: Where you implement your compiler

To submit your compiler, use Gradescope's GitHub integration to upload the branch of your team repo that you wish to be graded. Gradescope will run the corresponding test suites and give you a score. We have also provided Docker to allow you to run the autograder yourself in an environment identical to that on Gradescope. More detailed instructions will be on the Lab 1 writeup.

### Choosing a Language

- OCaml: If you don't have any particular desire to use another language, you should use OCaml. The majority of students choose this language each semester. It's a functional language that is very similar to SML but has many more standard libraries that make compiler implementation easier.
- Rust: It has native support for functional features while being a systems language that you can really optimize. However, Rust may not be as easy to pick up as OCaml, so we recommend using it only if you have some prior experience.
- Other: If you choose another language, make sure that you are very familiar with it and that it has the libraries and features you need to properly implement your compiler.

## Collaboration

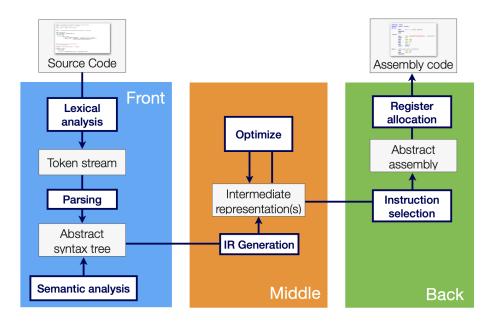
Here are a few tips for getting started collaborating on Lab 1 with your partner:

- You absolutely need to set up at least two meetings per week with your partner. Expect each of the meetings to last at least two hours. Use this time to discuss the overall architecture of your implementation and divide up the work.
- In your first meeting, you should spend a lot of time talking about your collaboration styles and making plans of attack.
- Use GitHub pull requests to read and review your partner's code. One partner makes a pull request, and the other reads it, makes comments, and eventually merges it into master.
- It might be tempting to divide the work between frontend and backend, but this is usually a bad idea because (1) the backend will require more work than the frontend and (2) it will be harder for both partners to gain knowledge of the entire system.
- Since Lab 1 is significantly easier than the later labs, it is **highly recommended** that you spend time on your register allocator. You'll save time later on and be able to focus on other parts of the compiler. Lab 1 checkpoint is intended to help you with this.

#### x86 Assembly - Resources

The backend of your compiler will demand a lot of work with x86 assembly. There are a lot of resources out there to help you with assembly, so we encourage you to search around! We also recommend these sites:

- https://www.felixcloutier.com/x86/: Very good instruction reference
- https://godbolt.org/: gcc compiler explorer that lets you input C code, and shows you the assembly produced by gcc. Very useful for debugging the assembly produced by your own compiler!



## **Compiler** Phases

In this recitation, we're going to introduce a simplified overview of what the compiler phases might look like for compiling a short piece of code. This is very similar to how the starter code structures the compiler.

Before we begin, recall the diagram of the compiler phases above.

#### Translation to Intermediate Representations

Since you won't have to touch the frontend for Lab 1 (unless you opt to not use any starter code), we'll leave those details for a future week. For now, we'll focus on the middle and backend. Here's the example code snippet and AST:

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```
1 int main() {
    int x = 42;
 2
    int z;
 3
 4
    if (x % 2 == 0) {
5
       x++;
       z = 1;
6
7
    } else {
8
       z = -1;
9
    }
    int y = 1;
10
11
    return z * y;
12 }
```

```
1 declare(x, seq(
   assign(x, const(42)),
2
    declare(z, seq(
3
       if(compare(mod(x, const(2)), const
4
           (0), EQ), seq(
          incr(x),
5
6
          assign(z, const(1))
7
       ),
8
          assign(z, neg(const(1)))
9
       ),
       declare(y, seq(
10
          assign(y, const(1)),
          return(times(z,y))
       ))
    ))
15))
```

e	trans-expr $(e)$	s	trans-stmt(s)
		$seq(c_1, c_2)$	$trans-stmt(c_1); trans-stmt(c_1)$
		declare(x,c)	trans-stmt(c)
x		assign(x, e)	x = trans-expr(e)
neg(e)	0 - trans-expr(e)	incr(x)	trans-stmt(assign(x, x+1))
$e_1 \oplus e_2$	$trans-expr(e_1)\oplustrans-expr(e_2)$	return(e)	return trans- $expr(e)$

We will now use the following rules to transform the AST into an intermediate representation:

# Checkpoint 0

Translate the above AST into an intermediate representation using the given translation rules. What do you notice about this IR in comparison to the original code?

Solution:

```
x = 42;
1
2
    t1 = x % 2;
    if(t1 == 0,
3
4
       x = x + 1;
5
           z = 1,
        z = 0 - 1
6
7
    );
8
    y = 1;
    return z * y;
9
```

# Code Generation: Tracing Through the Backend

Now lets convert this intermediate representation to abstract assembly. As discussed in lecture, we use the "maximal munch" algorithm to generate abstract 3-address assembly from IR. For each line in the IR, we recursively pattern-match as deep as possible into each sub-expression and generate lines of assembly at each step. The top-down formulation of the rules is shown as follows:

e	codegen-expr(d,e)	
c	$d \leftarrow c$	
x	$d \leftarrow x$	1  -
$e_1 \oplus e_2$	codegen-expr $(t_1, e_1)$ , codegen-expr $(t_2, e_2)$ , $d \leftarrow t_1 \oplus t_2$	] L

s	$codegen\operatorname{-stmt}(s)$
x = e	codegen-expr(x,e)
return $e$	$codegen\operatorname{expr}(\mathtt{\%rax}, e), ret$

A bottom-up approach can also work:

e	codegen-expr(e)	up		S	$codegen\operatorname{-stmt}(s)$
c		c		x = e return $e$	$\frac{x \leftarrow codegen-expr(e)}{\texttt{%rax} \leftarrow codegen-expr(e), ret}$
x		x	-		
$e_1\oplus e_2$	$t \leftarrow codegen-expr(e_1) \oplus codegen-expr(e_2)$	t			

In the former, the destination temporary is supplied as an argument to codegen-expr. In the latter, it is returned by codegen-expr.

### **Checkpoint 1**

Translate the above AST into abstract 3-address assembly using the bottom-up approach. You do not need to turn it into SSA form here. Answers may differ depending on the exact rules you're using in your head to generate instructions.

Solution:

```
1 main:
2 x <- 42
3 t2 <- x % 2
4 t1 <- t2
  if (t1 == 0) then goto L1 else goto L2
5
6 L1:
   t3 <-- x + 1
7
   x <- t3
8
9
   z <- 1
10 goto L3
11 L2:
12 t4 <- 0 - 1
13 z <- t4
14 goto L3
15 L3:
16 y <- 1
17 t5 <-- z * y
18 %rax <- t5
19
  ret
```