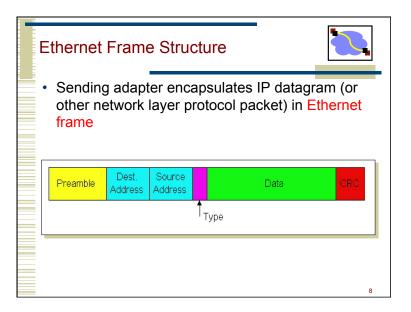
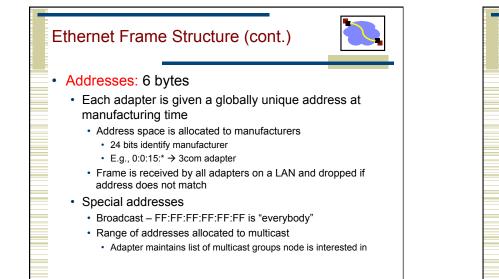


Ethernet Collision Detect



- Min packet length > 2x max prop delay
 - If A, B are at opposite sides of link, and B starts one link prop delay after A
- Jam network for 32-48 bits after collision, then stop sending
 - · Ensures that everyone notices collision







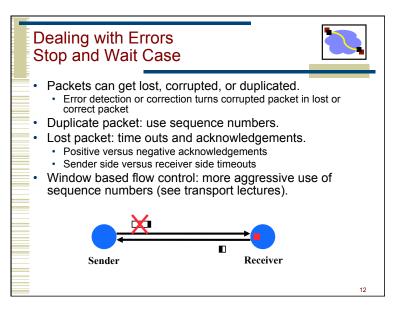
- Data coded as symbols of 5 line bits → 4 data bits, so 100 Mbps uses 125 MHz.
 - Uses less frequency space than Manchester encoding
- Uses NRI to encode the 5 code bits
- Each valid symbol has at least two 1s: get dense transitions.
- 16 data symbols, 8 control symbols
 - Data symbols: 4 data bits
 - · Control symbols: idle, begin frame, etc.
- Example: FDDI.

Framing

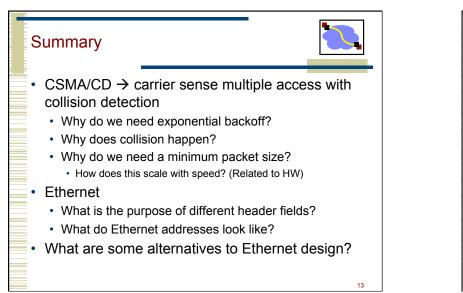


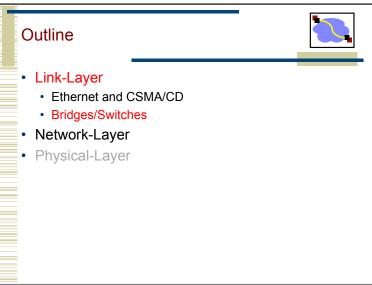
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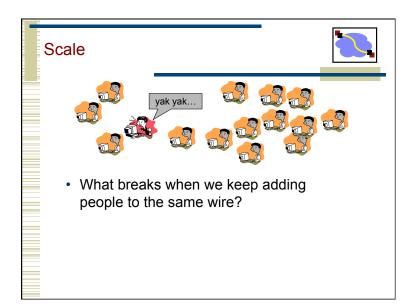
- A link layer function, defining which bits have which function.
- Minimal functionality: mark the beginning and end of packets (or frames).
- Some techniques:
 - out of band delimiters (e.g. FDDI 4B/5B control symbols)
 - frame delimiter characters with character stuffing
 - · frame delimiter codes with bit stuffing
 - synchronous transmission (e.g. SONET)

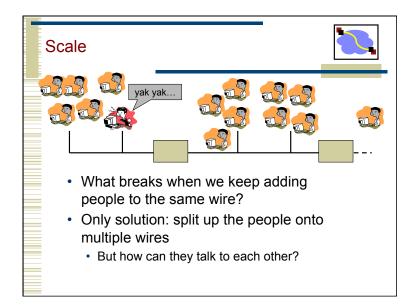


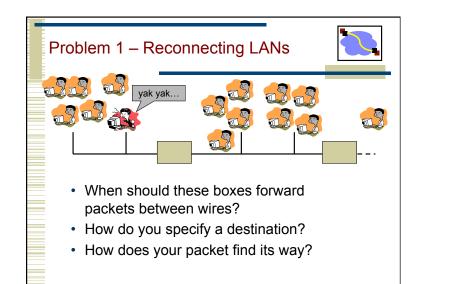
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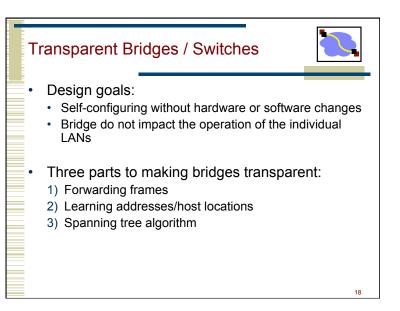


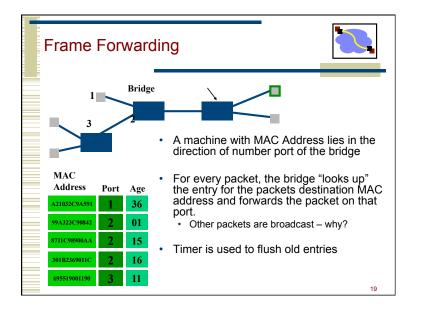


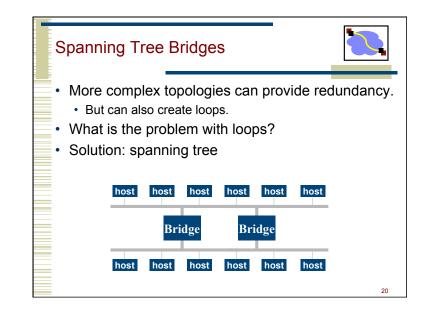


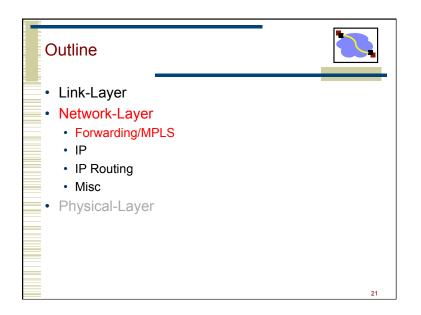


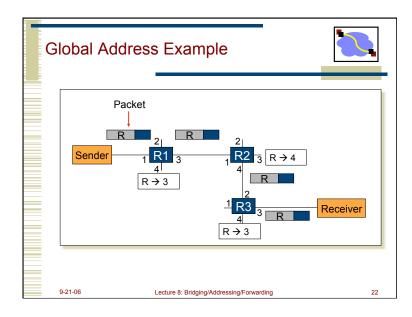


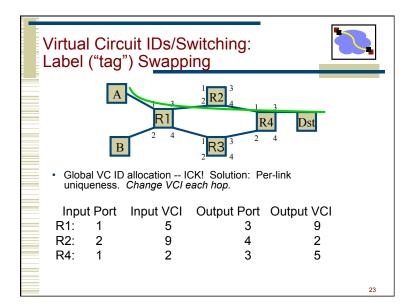


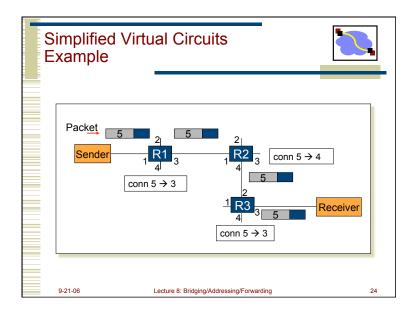




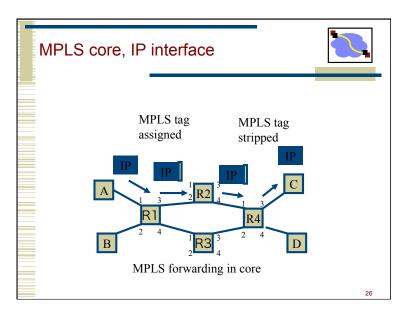


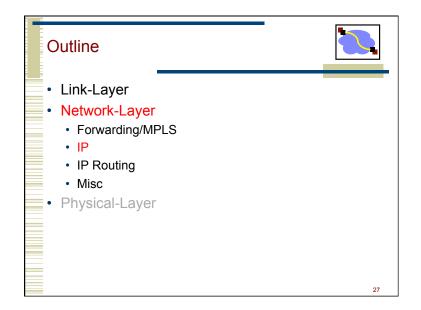






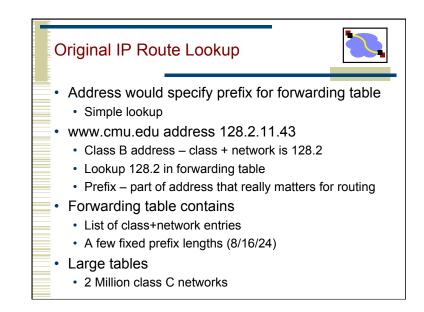
| Comparison | | | |
|-------------------|------------------|-------------------------------|---|
| | Source Routing | Global Addresses | Virtual Circuits |
| Header Size | Worst | OK – Large address | Best |
| Router Table Size | None | Number of hosts (prefixes) | Number of circuits |
| Forward Overhead | Best | Prefix matching (Worst) | Pretty Good |
| Setup Overhead | None | None | Connection Setup |
| Error Recovery | Tell all hosts | Tell all routers | Tell all routers and Tear down circuit and re-route |
| | | | |
| 9-20-07 | Lecture 7: Addre | ssing/Forwarding | |

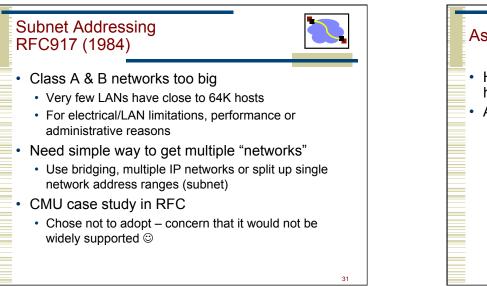


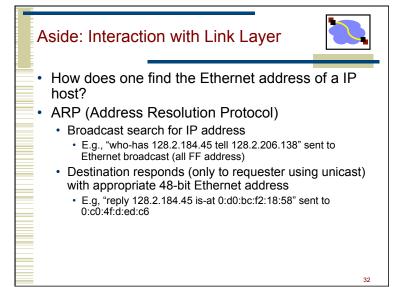


| IP Addresses | S | | | |
|---|---|-----------------------------|--|--|
| Fixed length: 32 bits Initial classful structure (1981) (not relevant now!!!) Total IP address size: 4 billion Class A: 128 networks, 16M hosts Class B: 16K networks, 64K hosts Class C: 2M networks, 256 hosts | | | | |
| High Order Bits 0 10 110 | Format 7 bits of net, 24 bits of host 14 bits of net, 16 bits of host 21 bits of net, 8 bits of host | <u>Class</u> A B C | | |
| | | 28 | | |

| P Address Classes Some are Obsolete) | |
|---|----|
| Network ID Host ID | |
| Class A 0 Network ID Host ID | 32 |
| Class B 10 | |
| Class C 110 | |
| Class D 1110 Multicast Addresses | |
| Class E 1111 Reserved for experiments | |
| | |







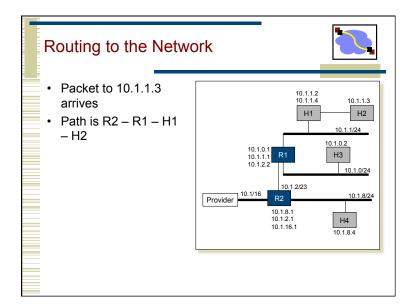


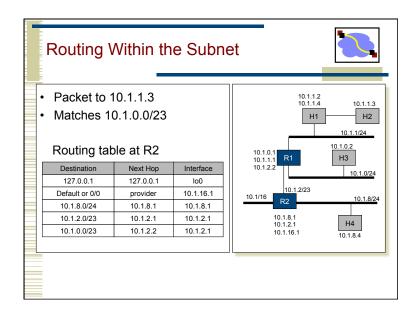


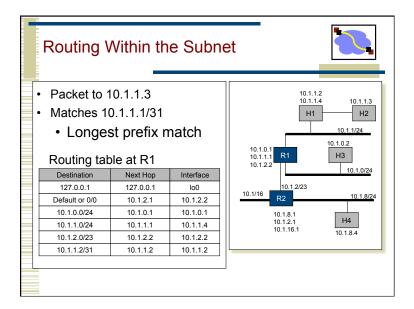
33

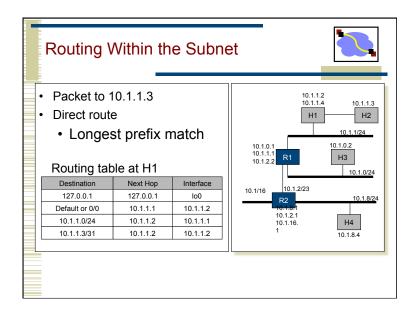
- Allows arbitrary split between network & host part of address
 - Do not use classes to determine network ID
 - · Use common part of address as network number
 - E.g., addresses 192.4.16 192.4.31 have the first 20 bits in common. Thus, we use these 20 bits as the network number → 192.4.16/20
- Enables more efficient usage of address space (and router tables) → How?
 - Use single entry for range in forwarding tables
 - · Combined forwarding entries when possible

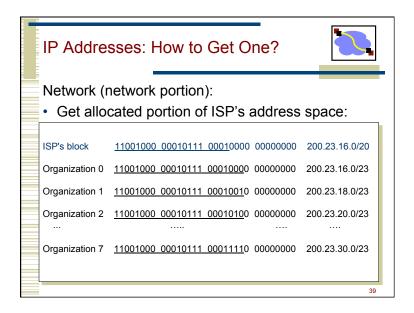
| ost Routing | Table Exam | ple | |
|--|---|---|----------------|
| Destination | Gateway | Genmask | Iface |
| 128.2.209.100 | 0.0.0.0 | 255.255.255.255 | eth0 |
| 128.2.0.0 | 0.0.0.0 | 255.255.0.0 | eth0 |
| 127.0.0.0 | 0.0.0.0 | 255.0.0.0 | 10 |
| 0.0.0.0 | 128.2.254.36 | 0.0.0.0 | eth0 |
| Dest 128.2.209 Dest 128.2.0.0 Dest 127.0.0.0 Dest 0.0.0.0 → | 0.100 when plu 0.100 → routing → other hosts → special loop default route | gged into CS et g to same machi on same ethern oback address to rest of Interne s.cmu.edu (128.2.2 | ne iet t |

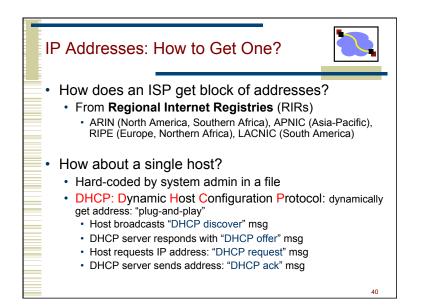


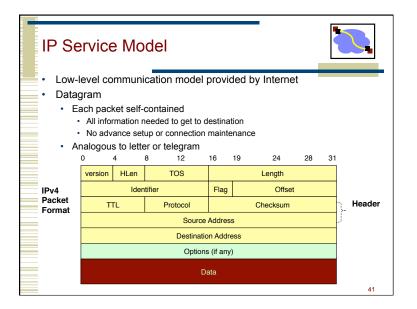


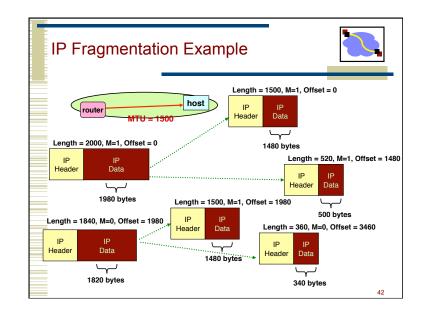


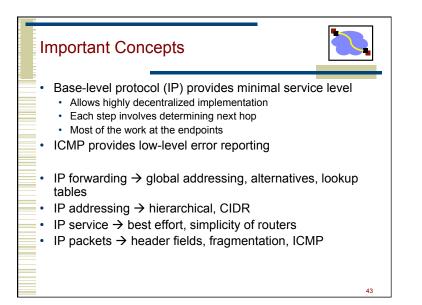


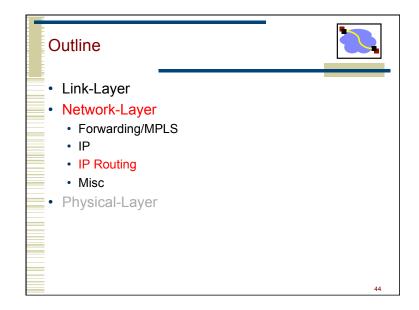


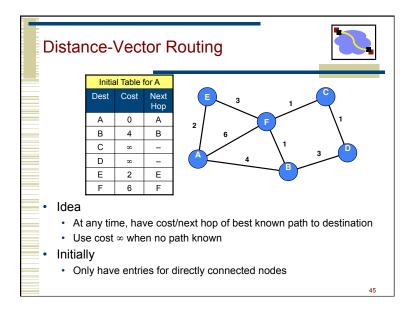


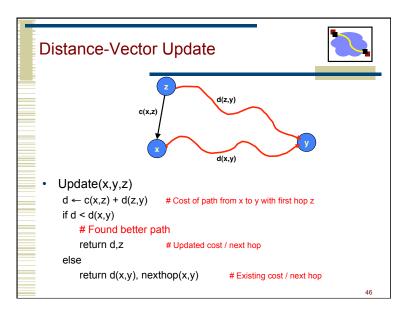


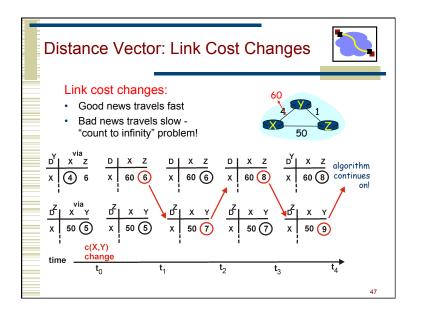


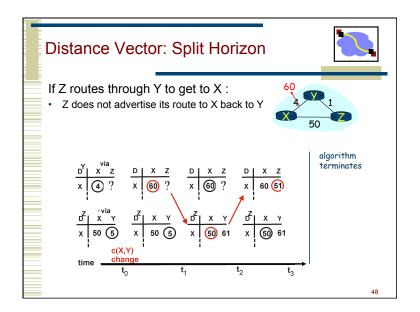


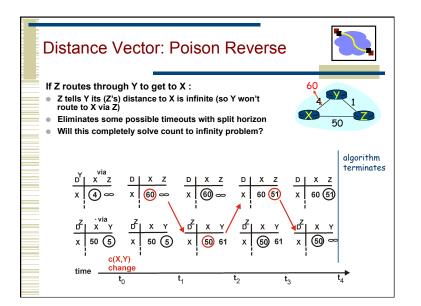


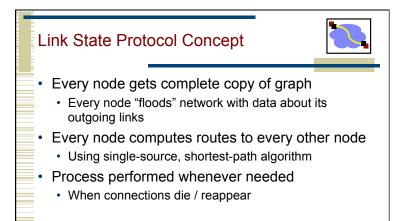


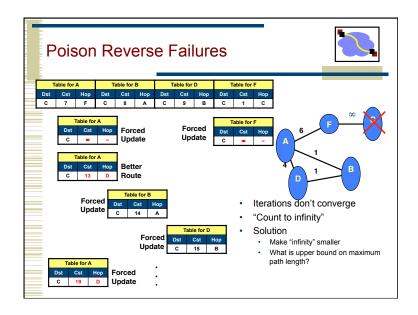


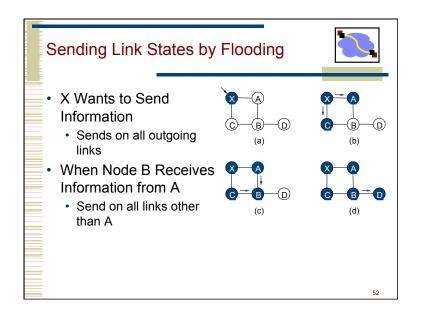


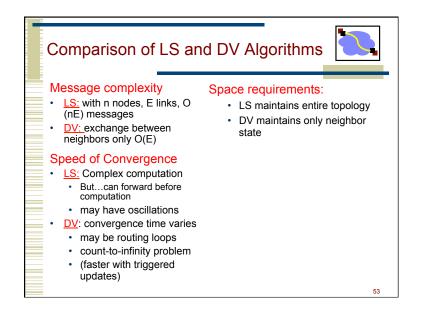


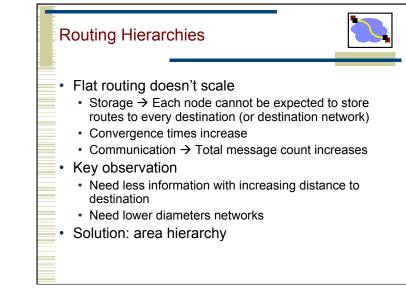


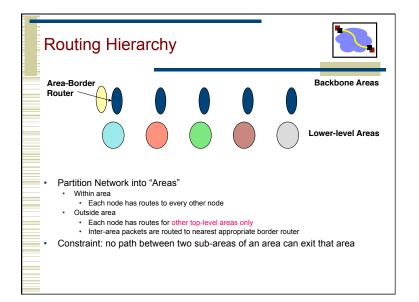


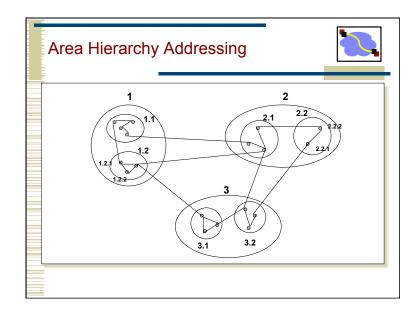


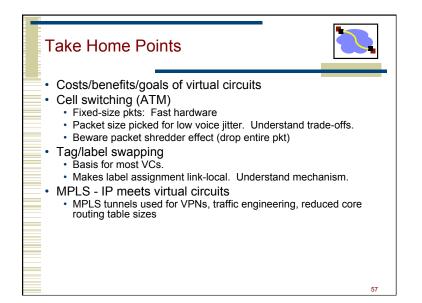


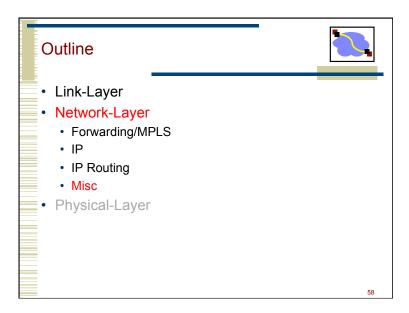


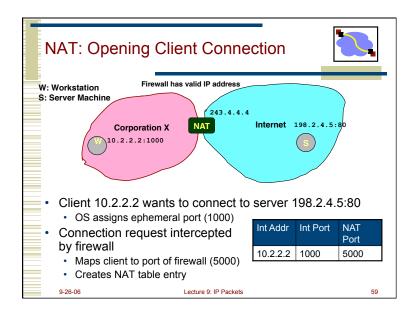


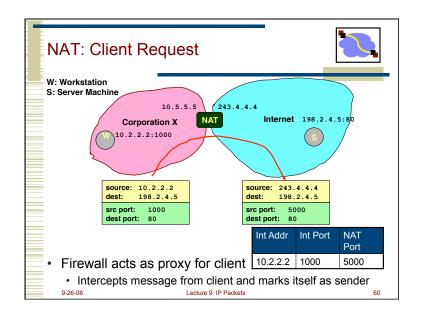


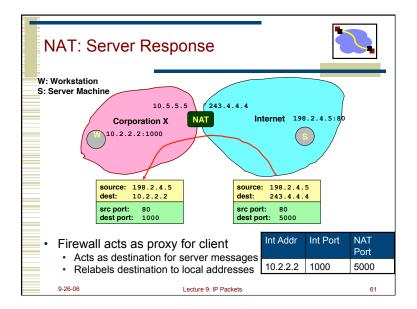


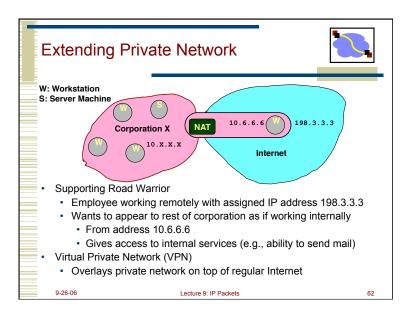


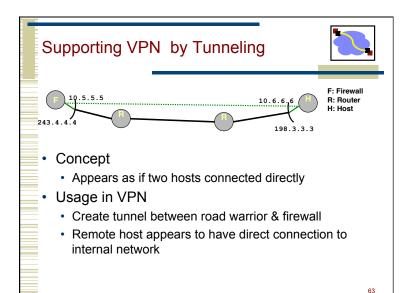


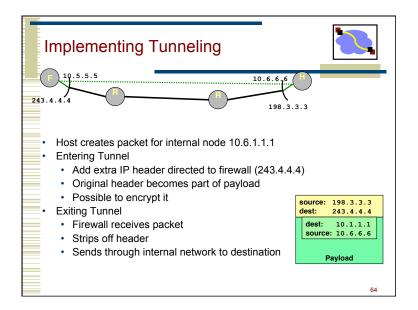


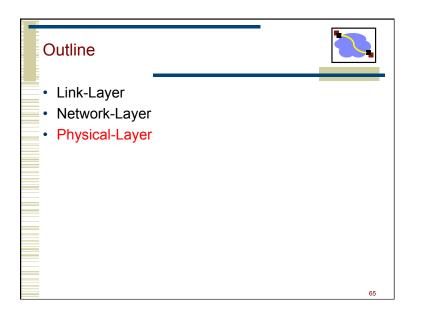


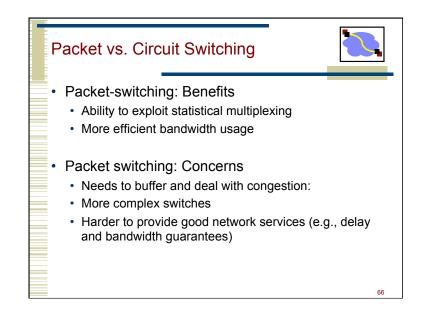


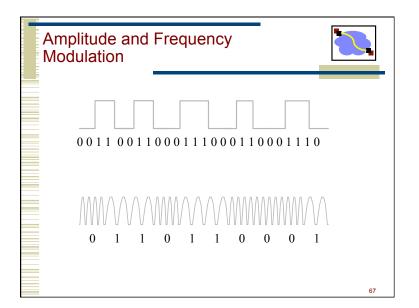


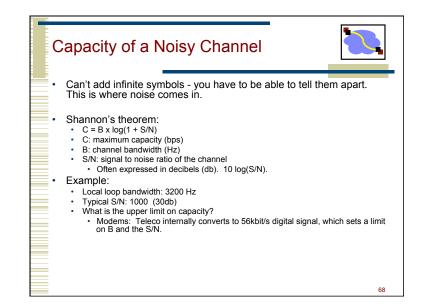


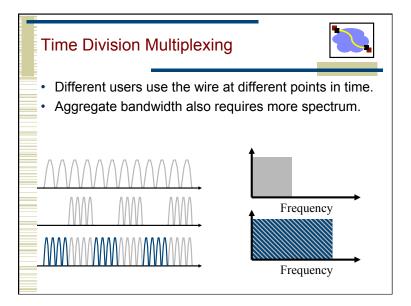


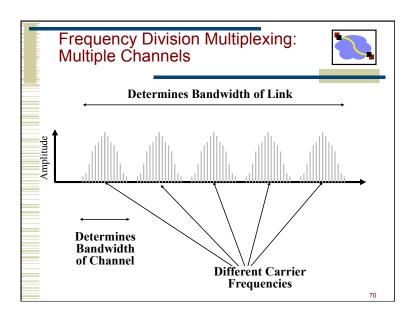


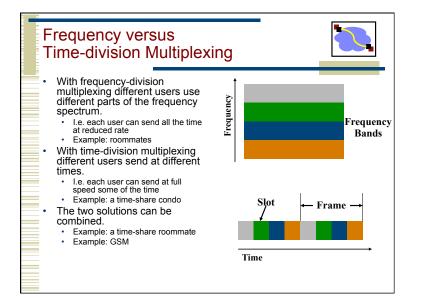












| From Signals | From Signals to Packets | | | |
|------------------------|-------------------------------------|--|--|--|
| Analog Signal | | | | |
| "Digital" Signal | | | | |
| Bit Stream | 0 0 1 0 1 1 1 0 0 0 1 | | | |
| Packets | Header/Body Header/Body Header/Body | | | |
| Packet Transmission | Sender Receiver | | | |

